



# SYNERGY INSTITUTE OF TECHNOLOGY, BHUBANESWAR

Approved by AICTE, New Delhi, Affiliated to BPUT, Odisha

## Department of Computer Science and Engineering

**Subject: Mathematics-1**

**Year/Semester: 1<sup>st</sup>/1<sup>st</sup>**

**Course Name: C101**

MODULE	CO STATEMENT After completion of the course the student will be able to	BTL
<b>Module 1:</b> Applications of definite integrals to evaluate length of curves, areas of surfaces and volumes of surfaces of revolution ,Improper integral (Definition and Elementary Examples) Beta and Gamma functions and their properties.	<b>CO101.1-</b> Learn the technique of calculus	L5
	<b>CO101.2</b> –Familiarize with gamma and beta function .	L2
<b>Module 2;</b> Mean value theorem (Statement and applications), First derivative test for local extreme values of functions. Power series, Taylor and Maclaurin series.	<b>CO101.3-</b> Apply of mean value theorem and power series	L3
<b>Module 3:</b> Partial derivatives. Jacobians, Hessian Matrix. Maxima, Minima and saddle points. Method of Lagrange multipliers.	<b>CO101.4</b> – Identify the application of partial derivatives .	L4
<b>Module 4:</b> Vector Space, Basis and dimension, Linear Systems of Equations, Gauss elimination, Linear Dependence and Independence, Rank of a Matrix.	<b>CO101.5-</b> Deal with linear system of equation and rank of a matrix .	L4
<b>Module 5:</b> Inverse of a matrix (Gauss-Jordan). Symmetric, skew-symmetric and orthogonal matrices. Eigen values and eigenvectors. Caley-Hamilton Theorem (Statement only)	<b>CO101.6-</b> Identify the specific properties of matrices .	L2



# SYNERGY INSTITUTE OF TECHNOLOGY, BHUBANESWAR

Approved by AICTE, New Delhi, Affiliated to BPUT, Odisha

## Department of Computer Science and Engineering

**Subject: Physics**

**Year/Semester: 1<sup>st</sup>/1<sup>st</sup>**

**Course Name: C102**

MODULE	CO STATEMENT After completion of the course the student will be able to	BTL
<b>Module1:</b> Mechanical and electrical simple harmonic oscillators, damped harmonic oscillator – heavy, critical and light damping, energy decay in a damped harmonic oscillator, quality factor, forced mechanical and electrical oscillators, steady state motion of forced damped harmonic oscillator	<b>CO102.1:</b> Apply knowledge of harmonic oscillation in mechanical and electrical system.	L3
<b>Module2:</b> Concept of wave and Wave equation, Superposition of many harmonic waves, Concept of coherent sources (Division of wave front and division of amplitude) . Interference in thin parallel film, Newton's ring: Determination of wavelength of light, Refractive index of liquid).Concept of diffraction (Huygen's Principle), Types of Diffraction, Fraunhofer diffraction due to single slit, diffraction grating (qualitatively).	<b>CO102.2:</b> Understand the basic concept of nature of light and the concept of coherent sources of waves.	L2
	<b>CO102.3:</b> Explain the thickness of the thin film , refractive index & resolving power of grating using principle of interference & diffraction of light	L2
<b>Module3: Vector calculus:</b> Gradient, Divergence, Curl (Mathematical concept), Gauss divergence theorem and Stoke's theorem (statement only), Derivation of Maxwell's electromagnetic equation in differential form and integral form, Electromagnetic wave equations for <b>E</b> and <b>B</b> in vacuum and conducting medium, transverse nature of EM waves.	<b>CO102.4:</b> Use Maxwell's equations & time varying electric field to show the nature of propagation of EM waves & its energy through free space, non-conducting and conducting media.	L4
<b>Module4:</b> Wave particle duality, concept of phase velocity group velocity, relation between them, Matter waves (de Broglie hypothesis), Wave functions, Observables as operators, Eigen function and Eigen values, Normalization, Expectation values, Schrodinger equation (Time dependent and time independent), Particle in a box.	<b>CO102.5:</b> Understand the Schrodinger wave equation to calculate the matter wave energy and momentum & particle in a box.	L2
<b>Module5:</b> Introduction to Laser, Characteristics of Lasers, Einstein's coefficients and relation between them, Lasing action, Population inversion, Three and four level pumping schemes, Ruby Laser, He-Laser.	<b>CO102.6:</b> Classify among different types of LASER and its transition probabilities.	L3



# SYNERGY INSTITUTE OF TECHNOLOGY, BHUBANESWAR

Approved by AICTE, New Delhi, Affiliated to BPUT, Odisha

## Department of Computer Science and Engineering

SYNERGY-BHUBANESWAR

**Subject: Basic Electrical Engineering**

**Year/Semester: 1<sup>st</sup>/1<sup>st</sup>**

**Subject Code: C103**

CO	CO STATEMENTS	BTL
	<b>After completion of the course the student will be able to</b>	
CO103.1	Implement principles of DC network, theorems & its application.	L2
CO103.2	Analyze the concept of Single phase and three phase AC circuits in different passive loads.	L3
CO103.3	Express the concept of magnetic circuit compression with electric ckt & control of hysteresis & eddy current loss & its prevention	L2
CO103.4	Apply basic principles of AC machines and their working.	L3
CO103.5	Apply basic principles of AC static & Dynamic machines and their working.	L3
CO103.6	Know different power plant and its detail domestic utilization.	L2



# SYNERGY INSTITUTE OF TECHNOLOGY, BHUBANESWAR

Approved by AICTE, New Delhi, Affiliated to BPUT, Odisha

## Department of Computer Science and Engineering

SYNERGY-BHUBANESWAR

**Subject: Programming C & Data Structure**

**Year/Semester: 1<sup>st</sup>/1<sup>st</sup>**

**Subject Code: C104**

MODULE	CO STATEMENT After completion of the course the student will be able to	BTL
<b>Module 1:</b> Fundamentals of C: Algorithms and Flow Chart. C as a Middle-level language, Structure of C Program, Character set Identifiers, Keywords, Data Types, Constant and Variables, Statements, Input and Output statements, Operators and Expressions, Precedence of operators, Control Structures	<b>CO104.1:</b> Understand the basic concept of programming language.	L2
	<b>CO104.2:</b> Understand fundamentals concept of C Programming & Implement simple algorithm to C program.	L3
<b>Module 2:</b> Function, Array, Structure and Union.	<b>CO104.3:</b> Test and Execute programs using Function, Array, Structure and Union	L3
<b>Module 3:</b> Pointer & Dynamic Memory Allocation. Call by value vs. Call by reference, Passing parameters, pointer to pointer, pointer to function, Pointer to Structure, Array and pointers, Static vs. Dynamic memory, Pointer variables, Dynamic memory allocation functions	<b>CO104.4:</b> Analyze the relation of memory and memory referencing with program execution.	L3
<b>Module 4:</b> Data Structures Introduction to Data Structure, Linear Linked List: Creation, Insertion, Deletion. Stack, Stack applications (Infix to postfix, postfix evaluation), Queue (linear & circular)	<b>CO104.5:</b> Apply different Data structure for Problem Solving.	L4
<b>Module 5:</b> Tree, Introduction to Sorting & Searching. Binary Tree, Binary Search Tree, Sorting, Searching (Linear Search, Binary Search)	<b>CO104.6:</b> Implement different Sorting and Searching Algorithm	L5



# SYNERGY INSTITUTE OF TECHNOLOGY, BHUBANESWAR

Approved by AICTE, New Delhi, Affiliated to BPUT, Odisha

## Department of Computer Science and Engineering

**Subject: Basic Civil Engineering**

**Year/Semester: 1<sup>st</sup>/1<sup>st</sup>**

**Subject Code: C105**

MODULE	CO STATEMENT After completion of the course the student will be able to	BTL
<b>Module-I:</b> Introduction to Civil Engineering: Various disciplines of Civil engineering, Importance of Civil engineering in infrastructure development of the country, interdisciplinary nature of construction projects. Residential Buildings: NBC Classification, Basic Components of a building: Basic requirement. Planning and Design of buildings: fundamental requirements, selection of sites, Introduction to building design: functional and structural design. Foundations: Classification, Bearing Capacity of Soil and related terms (definition only)	<b>CO105.1</b> - Understand the basics of civil engineering and fundamental aspects of building.	L1,L2
	<b>CO105.2</b> – Get an overview about classification of foundation and bearing capacity of soil	L1,L2
<b>Module-II:</b> Fundamental Properties of Construction Materials: Physical, mechanical and durability properties. Construction materials: stone, bricks, cement, aggregate, mortar, concrete, timber, steel, non-ferrous metals, paint, plastic, glass, adhesive, tiles, composites(Definition, classification and application)	<b>CO105.3</b> – Get brief overview of general aspects of building material.	L1,L2
<b>Module-III:</b> Importance of Transportation, Transportation modes i.e. Highway, railway, airways, water, pipe and conveyor – Basic Characteristics, advantages and disadvantages. Indian road transport system: Types of roads, classification of highway, urban roads: basic requirements and classification. Basic Components of a Road, Rigid and Flexible pavement (comparison only)	<b>CO105.4</b> - Get brief idea about transportation modes and planning.	L1,L2
<b>Module-IV:</b> Quantity of water: Sources of water, Per capita demand, drinking water standards, Public Water Supply System: Necessity and Basic lay out. Conventional water treatment process: Screening, Plain. Sedimentation, Sedimentation aided with Coagulation, Filtration, and Disinfection (working principles only).	<b>CO105.5</b> - Get brief idea about drinking water standards and water treatment plant.	L1,L2
<b>Module-V:</b> Irrigation: Importance of Irrigation, Classification of Irrigation projects, Irrigation system: Types, Field water distribution, Multipurpose river valley projects, Dams: Purpose, types. Layout of canal Irrigation system: components and definitions.	<b>CO105.6</b> - Get brief idea about irrigation network system.	L1,L2



# SYNERGY INSTITUTE OF TECHNOLOGY, BHUBANESWAR

Approved by AICTE, New Delhi, Affiliated to BPUT, Odisha

## Department of Computer Science and Engineering

**Subject: Universal Human Values**

**Year/Semester: 1<sup>st</sup>/1<sup>st</sup>**

**Subject Code: C106**

MODULE	CO STATEMENT	BTL
<b>MODULE I: FOUNDATIONS OF VALUE EDUCATION</b> -Right understanding, Relationship and Physical Facility (Holistic development and the Role of Education), Understanding Value Education.	<b>CO106.1:</b> Develop a holistic perspective based on self-exploration through value education.	<b>L6</b>
-Self-exploration as the Process for Value Education, Continuous Happiness and Prosperity, The Basic Human Aspirations, Happiness and Prosperity- Current Scenario, Method to Fulfill the Basic Human Aspirations.	<b>CO106.2:</b> Develop an understanding of the Self, the basic human aspirations and methods of fulfilling them.	<b>L6</b>
<b>MODULE II: HARMONY IN THE HUMANLIFE, RELATIONSHIPS AND SOCIETY.</b> -Understanding Human being as the Co-existence of the Self and the Body, distinguishing between the needs of the Self and the Body, Achieving Harmony:	<b>CO106.3:</b> Develop an understanding or gain clarity of the harmony in the human being as a co-existence of the Self and the Body.	<b>L2</b>
-Integrating Self and the Body, Harmony in the Family and Society, 'Trust' and 'Respect'-- as Foundational Values in Relationship, Other Feelings, Justice in Human-to-Human Relationship, Understanding Harmony in the Society and Universal Human Order.	<b>CO106.4:</b> Develop an understanding of the relationship with Family, society and nature/existence. Expanding the feeling of harmony from within to the same with the rest of the world.	<b>L6</b>
<b>MODULE III: HARMONY IN THE NATURE /EXISTENCE AND PROFESSIONAL ETHICS.</b> -Understanding Harmony in the Nature, Interconnectedness, self-regulation and Mutual Fulfillment among the Four Orders of Nature, Realizing Existence as Co-existence at All Levels, The Holistic Perception of Harmony in Existence, Natural Acceptance of Human Values, Humanistic Education, Humanistic Constitution and Universal Human Order.	<b>CO106.5:</b> Develop an understanding of the relationship of synthesis and mutual fulfillment with nature and the world order. An awareness of the eco-system, one's place in and responsibility towards the same.	<b>L2</b>
-Competence in Professional Ethics- Ethical Decision Making and Transition towards Value-based Life and Profession.	<b>CO106.6:</b> Adapt to the present-day work scenario with knowledge of professional ethics, decision making, progressive and sustainable living.	<b>L6</b>



# SYNERGY INSTITUTE OF TECHNOLOGY, BHUBANESWAR

Approved by AICTE, New Delhi, Affiliated to BPUT, Odisha

## Department of Computer Science and Engineering

**Subject: Physics Laboratory**

**Year/Semester: 1<sup>st</sup>/1<sup>st</sup>**

**Subject Code: C107**

CO No.	List of Experiments	Statement	BTL
C107.1	Determination of acceleration due to gravity by using Bar Pendulum	Analyze acceleration due to gravity by using Bar pendulum and resonance using sonometer	L3
	Study of resonance using sonometer for unknown frequency		
C107.2	Determination of wavelength of monochromatic light with the help of Newton's ring apparatus	Understand wavelength of monochromatic light by using Newton's ring apparatus and diffraction grating using spectrometer.	L2
	Determination of grating element of a diffraction grating using spectrometer		
C107.3	Study of RLC circuit	Analyze RLC circuit, characteristic of Bipolar Junction Transistor (BJT) and PN junction diode.	L3
	To draw the characteristics of a bipolar junction transistor		
	To draw the VI characteristics of a PN Junction diode		
C107.4	To determine the rigidity modulus of the material of a wire by using Barton's apparatus	Understand the rigidity modulus by using Barton's apparatus, Young's modulus by Searle's apparatus and surface tension of capillary rise method.	L2
	To determine the Young's modulus of a given material by Searle's apparatus		
	Determination of surface tension of water by capillary rise method		
C107.5	Magnetic field measurement from Helmholtz coil	Analyse the magnetic field of Helmholtz coil & e/m ratio.	L3
	To determine e/m ratio		



# SYNERGY INSTITUTE OF TECHNOLOGY, BHUBANESWAR

Approved by AICTE, New Delhi, Affiliated to BPUT, Odisha

## Department of Computer Science and Engineering

**Subject: Basic Electrical Engineering Lab**

**Year/Semester: 1<sup>st</sup>/1<sup>st</sup>**

**Subject Code: C108**

CO	After completion of the course the student will be able to	BTL
CO108.1	Express the safety rules as per ISS and symbols of different electrical components and the use of various electrical instruments in the laboratory.	L2
CO108.2	Demonstrate the working and operational characteristics of dc motor and dc generator.	L2
CO108.3	Evaluate the voltage, current, power and power factor of choke coil and study BH curve of a ferromagnetic core.	L2
CO108.4	Measure armature and field resistance of DC machines, earth resistance and insulation resistance and demonstrate the internal structure of different machines.	L2
CO108.5	Analyze the connection and calibration of single phase energy meter.	L3



# SYNERGY INSTITUTE OF TECHNOLOGY, BHUBANESWAR

Approved by AICTE, New Delhi, Affiliated to BPUT, Odisha

## Department of Computer Science and Engineering

**Subject: Programming Lab**

**Year/Semester: 1<sup>st</sup>/1<sup>st</sup>**

**Subject Code: C109**

MODULE	CO STATEMENT After completion of the course the student will be able to	BTL
1. Write a program to print your Bio-data. 2 Write a program in C to test the arithmetic operators. 3 Write a program to find out the simple interest and compound interest with the given input data. 4. Write a program to enter the marks of a student in 4 subjects. Then calculate the total, Aggregate %, and display the grades obtained by the student. 5. Write a program to print the following pyramid star pattern.	<b>CO109.1-</b> Develop C programs for simple applications making use of basic constructs.	<b>L6</b>
6. Write a program to check a given number is palindrome or not. 7. Write a program to enter a number from 1-7 and display the corresponding day of the week using switch case statement. 8. Write a program to generate prime numbers present between two given numbers.	<b>CO109.2-</b> Develop C programs involving Loops	<b>L6</b>
8. Write a program that will accept an array, and find the largest number, smallest number, sum of the elements and average of the elements present in the array. 9. Write program that will accept an array and sort the array in ascending order. Display both the unsorted and sorted arrays. 10. Write a program that will insert an element at a desired position of an array. Show the array before insertion and after insertion of the new element (Array, element and position will be provided by the user)	<b>CO109.3-</b> Design and implement applications in C using Arrays and Strings	<b>L6</b>
11. Write a program to swap the value of two inputted variable using function. Show the initial value and value after swapping. 12 Write a program to print the Fibonacci series using function. 13 Write a program that will accept two matrices using function and multiply them using function and show the result using function. 14. Write a program to find the GCD among two given number using recursion. 15. Check a inputted string is palindrome or not using pointer.	<b>CO109.4-</b> Develop C programs involving Functions, Recursion, and Pointers.	<b>L6</b>
16. Write a program to implement insertion and deletion of an element using linked list. 17. Write a program to implement Push and Pop operations in Stack. 18. Write a program to implement insert and delete operations in Queue.	<b>CO109.5-</b> Develop C programs involving Linked List, Stack and Queue	<b>L6</b>
19. Write a program to implement Quick Sort algorithm using C. 20. Write a program to search an element using Linear Search algorithm. 21. Write a program to search an element using Binary Search algorithm.	<b>CO109.6-</b> Develop C programs involving searching and sorting	<b>L6</b>



# SYNERGY INSTITUTE OF TECHNOLOGY, BHUBANESWAR

Approved by AICTE, New Delhi, Affiliated to BPUT, Odisha

## Department of Computer Science and Engineering

SYNERGY-BHUBANESWAR

**Subject: Engineering Graphics & Design Lab**

**Year/Semester: 1<sup>st</sup>/1<sup>st</sup>**

**Subject Code: C110**

Expt. No.	Experiment Name	CO Statement After completion of the course the student will be able to	BTL
1	<b>AUTO CAD:</b> layout of the software, standard tool bar/menus and description of most commonly used toolbars, navigational tools. Co-ordinate system and reference planes. Definitions of HP, VP, RPP & LPP. Creation of 2D/3D environment. Selection of drawing size and scale. Commands and creation of Lines, Co-ordinate points, axes, poly-lines, square, rectangle, polygons, splines, circles, ellipse, text, move, copy, off-set, mirror, rotate, trim, extend, break, chamfer, fillet, curves, constraints.	<b>CO110.1</b> Study different coordinate system, commands of Auto CAD and creation of basic drawings.	L1,L2,L3
2	<b>Orthographic Projections:</b> Introduction, Definitions - Planes of projection, reference line and conventions employed, Projections of points in all the four quadrants, Projections of straight lines (located in First quadrant/first angle only), True and apparent lengths, True and apparent inclinations to reference planes.	<b>CO110.2</b> Study orthographic projection of point, straight line and planes.	L1,L2,L3
3	<b>Orthographic Projections of Plane Surfaces (First Angle Projection Only):</b> Introduction, Definitions—projections of plane surfaces—triangle, square, rectangle, rhombus, pentagon, hexagon and circle, planes in different positions by change of position method only	<b>CO110.3</b> – Study projection of different solids in first angle concept.	L1,L2,L3
4	<b>Projections of Solids (First Angle Projection Only):</b> Introduction, Definitions – Projections of right regular tetrahedron, hexahedron (cube), prisms, pyramids, cylinders and cones in different positions. 2-Sheets Sections and Development of Lateral Surfaces of Solids Introduction, Section planes, Sections, Section views, Sectional views, Apparent shapes and True shapes of Sections of right regular prisms, pyramids, cylinders and cones resting with base on HP.		
5	<b>Isometric Projection (Using Isometric Scale Only):</b> 21 Introduction, Isometric scale, Isometric projection of simple plane figures, Isometric projection of tetrahedron, hexahedron(cube), right regular prisms, pyramids, cylinders, cones, spheres, cut spheres.	<b>CO110.4</b> – Understand isometric projection of different solid objects.	L1,L2,L3



# SYNERGY INSTITUTE OF TECHNOLOGY, BHUBANESWAR

Approved by AICTE, New Delhi, Affiliated to BPUT, Odisha

## Department of Computer Science and Engineering

SYNERGY-BHUBANESWAR

**Subject: Mathematics-II**

**Year/Semester: 1<sup>st</sup>/ 2<sup>nd</sup>**

**Subject Code: C111**

MODULE	CO STATEMENT After completion of the course the student will be able to	BTL
Exact ODEs. Integrating factors. Linear first order ODEs. Nonlinear first order ODE and Bernoulli's equations, Applications to Population growth, Newton's law of cooling, RL circuit.	<b>CO111.1-</b> Solve first order differential equation analytically using standard method .	L3
Second order linear differential equations with constant coefficients, Euler-Cauchy equations, method of undetermined coefficients, solution by variation of parameters. Power series solutions of ODE. Legendre's equations (explicit solution only).	<b>CO112.2</b> –Familiarize with higher order differential equation.	L4
Vector and Scalar Functions and Fields, Derivatives, Gradient of a Scalar Field, Directional Derivative, Divergence of a Vector Field, Curl of a Vector Field,	<b>CO112.3-</b> Illustrate the concept of vector differential calculus.	L3
Line Integrals, Path Independence of Line Integrals, Double Integrals, Green's Theorem in the Plane (Statement and applications)	<b>CO112.4</b> Illustrate the concept of line integral c.	L4
Limit, Continuity, Derivative, Analytic Function, Cauchy-Riemann Equations, Laplace's Equation, Exponential Function, Trigonometric and logarithm functions.	<b>CO112.5</b> Know about various complex functions.	L3
Line Integral in the Complex Plane, Cauchy's Integral Theorem, Cauchy's Integral Formula, Derivatives of Analytic Functions, Laurent series, Residue theorem with simple problems.	<b>CO112.6</b> Familiarize with the complex integration.	L2



# SYNERGY INSTITUTE OF TECHNOLOGY, BHUBANESWAR

Approved by AICTE, New Delhi, Affiliated to BPUT, Odisha

## Department of Computer Science and Engineering

**Subject: Chemistry**

**Year/Semester: 1<sup>st</sup>/ 2<sup>nd</sup>**

**Subject Code: C112**

MODULE	CO STATEMENT After completion of the course the student will be able to	BTL
Quantum Chemistry: Basic concepts and postulates of quantum mechanics. Introduction to Schrodinger Wave Equation (without derivation), Particle in a box: Energy levels, quantum numbers and selection rule	<b>CO112.1-</b> Demonstrate various periodic properties associated with different elements present in different groups and periods of modern periodic table	<b>L4</b>
Spectroscopy: Lambert Beer's Law, Principles and applications of UV-Visible Molecular Absorption Spectroscopy; Chromophores, applications on quantitative analysis. Effect of conjugation on chromophores, Absorption by aromatic systems, introductory idea on rotational and vibrational Spectroscopy Principles and application to diatomic molecules.	<b>CO112.2-</b> Understand about free energy concept of the thermodynamics associated with chemical reaction and equilibrium	<b>L1</b>
The phase rule: Statement of Gibb's phase rule and explanation of the terms involved, Phase diagram of one component system – water and sulfur system, Condensed phase rule, Phase diagram of two component system – Eutectic Bi-Cd, Pb-Tin system & Isomorphous System.	<b>CO112.3-</b> Analyze the concept of rotational and vibrational spectroscopy techniques for identification of organic and inorganic compounds	<b>L4</b>
Fuels: Classification of fuels, calorific value. (Determination by Dulong's formula), G.C.V. and N.C.V., Solid fuels, Analysis of coal. Liquid fuels: Classification of petroleum, Refining of petroleum, Cracking, Knocking and anti-knocking, cetane and octane numbers. Unleaded petrol, synthetic petrol, power alcohol. Gaseous Fuel: Producer gas, Water gas, LPG, CNG, Kerosene gas, Combustion calculation	<b>CO112.4-</b> Understand the concepts of UV spectroscopy for identification of organic and inorganic compounds.	<b>L2</b>
Corrosion: Electrochemical theory of corrosion, galvanic series, Types of corrosion; Differential metal corrosion, Differential aeration corrosion (Pitting and water line corrosion), Stress corrosion (caustic embrittlement in boilers), Factors affecting, metal coatings – Galvanizing and Timing, Corrosion inhibitors, cathodic protection.	<b>CO112.5-</b> Analyze the concept of configuration and conformations of various organic compounds .	<b>L4</b>
New Materials: Introduction to nano materials, classification (0D, 1D, 2D) with examples, size dependent properties, Top-down and Bottom-up approaches of nanomaterial synthesis. Introductory idea on synthesis of nano materials via green synthetic	<b>CO112.6-</b> Understand the generation , reaction , and identification of intermediate reaction and there applications .	<b>L2</b>



# SYNERGY INSTITUTE OF TECHNOLOGY, BHUBANESWAR

Approved by AICTE, New Delhi, Affiliated to BPUT, Odisha

## Department of Computer Science and Engineering

SYNERGY-BHUBANESWAR

**Subject: Basic Electronics**

**Year/Semester: 1<sup>st</sup>/ 2<sup>nd</sup>**

**Subject Code: C113**

<b>MODULE</b>	<b>CO STATEMENT After completion of the course the student will be able to</b>	<b>BTL</b>
Semiconductor Physics: Properties of semiconductor, current flow in semiconductors, voltage -current characteristic of a p-n junction, Rectifiers Bipolar junction Transistor (BJT): Device structure, types and modes of operation, static characteristic, BJT as a switch, BJT as an amplifier, concept of biasing of BJT	<b>CO113.1:</b> Know basics of semiconductor diode and Introduction to transistor with different biasing techniques.	<b>L2</b>
JFET: Physical structure, operation and static characteristics MOSFET: Physical structure, operation and characteristics of D- and E-type MOSFET Integrated Circuits: Introduction to CMOS technology in VLSI, Introduction to Integrated circuits, Fabrication of monolithic IC, Integration of circuit components, Limitations of VLSI	<b>CO113.2:</b> Understand different biasing techniques to operate FET, MOSFET and CMOS circuits in different circuit design.	<b>L2</b>
Feedback Amplifiers: General feedback structure, properties of negative feedback, four basic types of feedback topologies (Block diagram only) Operational Amplifier (OP-AMP): Ideal OP-AMP, inverting configuration, non-inverting configuration, OP-AMP Applications (Adder, Subtractor only)	<b>CO113.3:</b> Know operational amplifier in different modes and analyze the design of circuits using op-amp.	<b>L2</b>
Digital Electronics Fundamentals-Number system (Decimal, Binary, Octal and Hexadecimal), conversion among number systems, signed-binary numbers, binary addition, subtraction, multiplication and division, logic gates, laws of Boolean Algebra, simplification of expressions	<b>CO113.4:</b> Compare design, advantages, and limitations of basic feedback circuits. Compare design, advantages, disadvantages and limitations of basic digital electronics circuits.	<b>L3</b>
Electronic Instruments: Overview of CRO, DSO; principles of operation, waveform reconstruction	<b>CO113.5:</b> Know about CRO and basics of communication circuits.	<b>L2</b>
Comparison between CRO & DSO, applications of oscilloscope, Principles of Communication Systems: Fundamentals of AM & FM, (Waveforms and general expressions only)	<b>CO113.6:</b> Remember the fundamentals of different Digital arithmetic operations	<b>L2</b>



# SYNERGY INSTITUTE OF TECHNOLOGY, BHUBANESWAR

Approved by AICTE, New Delhi, Affiliated to BPUT, Odisha

## Department of Computer Science and Engineering

**Subject: Engineering Mechanics**

**Year/Semester: 1<sup>st</sup>/ 2<sup>nd</sup>**

**Subject Code: C114**

Module	CO Statement After completion of the course the student will be able to	BTL
<b>Module-I:</b> Concurrent forces on a plane: Composition, resolution and equilibrium of concurrent coplanar forces, method of moment. General case of forces on a plane: Composition and equilibrium of forces in a plane, plane trusses, method of joints and method of sections.	<b>CO114.1</b> - Ability to analyze objects in static equilibrium including the determination of reactions, forces and moments.	<b>L4</b>
<b>Module-II:</b> Friction: Fundamentals and Problems involving friction, Ladder, Wedges. Principle of virtual work.	<b>CO114.2</b> – Enrich fundamental concept of friction and demonstrate the analytical skills to solve the problems involving friction.	<b>L5</b>
<b>Module-III:</b> Parallel forces on a plane: General case of parallel forces, center of parallel forces and center of gravity, Centroid of plane and composite figures, Theorems of Pappus and Guildins. Moment of inertia: Plane figure with respect to an axis in its plane and perpendicular to the plane, Polar moment of inertia, parallel axis theorem.	<b>CO114.3</b> – To Understand the knowledge for determination of centroid and second moment of area of sections and their engineering applications.	<b>L2</b>
<b>Module-IV:</b> Rectilinear translation: Kinematics, Principle of dynamics, D'Alembert's Principle, Principle of work and energy for a particle and a rigid body, Conservation of energy, Principle of impulse and momentum for a particle and a rigid body, Conservation of momentum, System of rigid bodies, Impact, direct and central impact, coefficient of restitution	<b>CO114.4</b> -To Analyze the work done by forces, the energy transferred from one object to other and applies principle of work and energy conservation in realistic/practical engineering problems. <b>CO114.5</b> – To Study the influence of impulse and conservation of momentum on rigid bodies.	<b>L4</b>  <b>L1</b>
<b>Module-V:</b> Curvilinear translation: Kinematics, Equation of motion, Projectile, D'Alembert's principle of curvilinear motion. Kinematics of rotation of rigid body.	<b>CO114.6</b> - Identify the various parameters in projectile motion. Apply the principle of dynamics .	<b>L3</b>



# SYNERGY INSTITUTE OF TECHNOLOGY, BHUBANESWAR

Approved by AICTE, New Delhi, Affiliated to BPUT, Odisha

## Department of Computer Science and Engineering

SYNERGY-BHUBANESWAR

**Subject: Basic Mechanical Engineering**

**Year/Semester: 1<sup>st</sup>/ 2<sup>nd</sup>**

**Subject Code: C115**

Module	CO Statement After completion of the course the student will be able to	BTL
<b>Module-I:</b> Thermodynamics: Systems, Properties, Process, State, Cycle, Internal energy, Enthalpy, Zeroth Law, First law, and Second Law of Thermodynamics, Basic Concept Entropy, Properties of ideal gas, Properties of pure Substances, Enthalpy, Specific volume, Internal energy and dryness fraction of steam, use of Steam tables. Related numerical.	<b>CO115.1:</b> Comprehending the Laws of Thermodynamics and properties of steam.	<b>L1</b>
<b>Module-II:</b> Application of Thermodynamics: Single stage air compressor, Steam Power Plant, I.C. Engines (Brief Description on working principles with Schematic diagrams only) Elements of Fluid Mechanics and Heat Transfer Properties used in Fluid Mechanics, Fluid Statics, Kinematics and Dynamics (Concepts only), Heat transfer and Classifications (Concepts only)	<b>CO115.2:</b> Being Aware of how crucial thermodynamics is upon IC engines, power plants, refrigerators, and Heat Pump.  <b>CO115.3:</b> Being Aware of fluid mechanics and heat transfer concepts.	<b>L1</b>  L1
<b>Module-III:</b> Introduction to Manufacturing: Classification of engineering materials, Material Properties, Manufacturing processes: Welding, Casting, Forming (Basics only)	<b>CO115.4:</b> Understanding of Engineering materials and its properties  <b>CO115.5:</b> Have a fundamental understanding of welding, Casting, Forming techniques.	<b>L2</b>  L2
<b>Module-IV:</b> Basic Power transmission devices: Belt, Gear drives, clutch, brakes. (Working principle only) Introduction to Robotics: Robot anatomy, Joints and links and common robot configurations.	<b>CO115.6:</b> Recognize fundamentals concern to power transfer mechanisms and robotics system.	<b>L2</b>



# SYNERGY INSTITUTE OF TECHNOLOGY, BHUBANESWAR

Approved by AICTE, New Delhi, Affiliated to BPUT, Odisha

## Department of Computer Science and Engineering

**Subject: English For Technical Writing**

**Year/Semester: 1<sup>st</sup>/ 2<sup>nd</sup>**

**Subject Code: C116**

Module	CO Statement After completion of the course the student will be able to	BTL
<b>MODULE 1: FUNDAMENTALS OF TECHNICAL COMMUNICATION</b> Process of communication, Types of Communication (verbal & non-verbal), Channels of Business Communication, Barriers to Communication, Bias Free Language, Cross-cultural Communication	<b>CO116.1:</b> Understand the concept of communication and the objective of communication relevant for the Engineers.	L2
<b>MODULE II: COMMUNICATIVE GRAMMAR</b> Time and Tense, Passive and Active Voice, The English Conditionals.	<b>CO116.2:</b> Use suitable vocabulary and grammar with confidence and express their ideas both in speech and writing.	L3
<b>MODULE III: SOUNDS OF ENGLISH</b> Consonant sounds of English, vowel sounds of English, Stress patterns: Syllable, stress and Intonation, Problem sounds for Indian speakers.	<b>CO116.3:</b> Evaluate their efficacy as fluent and efficient communicators by learning the voice dynamics.	L2
<b>MODULE IV: PROFESSIONAL COMMUNICATION AT WORKPLACE</b> Paragraph Writing (the seven Cs of good professional writing), Formal letter writing, Memo and Notice writing, Agenda and Minute writing, Report writing.	<b>CO116.4:</b> Understand, develop and demonstrate appropriate and competent professional writing skills to communicate information effectively in the organization setup.	L3
<b>MODULE V: PROFESSIONAL COMMUNICATION AND EMPLOYMENT-CV Writing</b>	<b>CO116.5:</b> Formulate a comprehensive holistic and job specific chronological or functional resume, to increase employability.	L4
-Interview Skills	<b>CO116.6:</b> Develop appropriate skills to face interviews with confidence and to develop desired corporate and social etiquettes.	L3



# SYNERGY INSTITUTE OF TECHNOLOGY, BHUBANESWAR

Approved by AICTE, New Delhi, Affiliated to BPUT, Odisha

## Department of Computer Science and Engineering

SYNERGY-BHUBANESWAR

**Subject: Chemistry Laboratory**

**Year/Semester: 1<sup>st</sup>/ 2<sup>nd</sup>**

**Subject Code: C117**

<b>Experiment Name</b>	<b>CO Statement After completion of the course the student will be able to</b>	<b>BTL</b>
1. Determination of the alkalinity in the given water sample 2. Determination of the temporary and permanent hardness in the given water sample by complexometric titration using EDTA as standard solution.	<b>CO117.1:</b> Analyze the alkalinity and hardness value of the water sample for industrial application.	<b>L4</b>
3. Standardization of potassium permanganate using sodium oxalate 4. Determination of amount of ferrous iron present in Mohr's salt. 5. Estimation of calcium in Limestone	<b>CO117.2:</b> Analyze the concentration of present in the solution. To analyze the concentration of Iron for industrial application.	<b>L1</b>
6. Determination of amount of available chlorine in bleaching powder. 7. Determination of dissolved oxygen in water sample.	<b>CO117.3:</b> Analyze the chlorine content and dissolve oxygen of water sample for industrial application.	<b>L4</b>
8. Synthesis of Aspirin/Paracetamol	<b>CO117.4:</b> Impact knowledge on preparation of drugs for industrial application.	<b>L2</b>
9. Determination of viscosity of lubricating oil by Redwood viscometer. 10. Determination of flash point of a given oil by Pensky-Martens flash point apparatus.	<b>CO117.5:</b> Analyze viscosity and flashpoint of lubricating oils. For industrial application.	<b>L4</b>



# SYNERGY INSTITUTE OF TECHNOLOGY, BHUBANESWAR

Approved by AICTE, New Delhi, Affiliated to BPUT, Odisha

## Department of Computer Science and Engineering

SYNERGY-BHUBANESWAR

**Subject: Basic Electronics Lab**

**Year/Semester: 1<sup>st</sup>/ 2<sup>nd</sup>**

**Subject Code: C118**

Experiment Name	CO Statement After completion of the course the student will be able to	BTL
1. Familiarity with electronic components and devices (Testing of semiconductor diode, Transistor, IC Pins connection) Digital Multimeter should be used.	CO118.1: Acquire basic knowledge on electronic devices and components	L2
2. Study and use of CRO to view waveforms and measure its Amplitude and Frequency.		
3. V-I Characteristics of a Semiconductor Diode	CO118.2: Design different electronics circuits using semiconductor diodes.	L2
4. V-I (Output) Characteristics of N-P-N/P-N-P Transistor in CE Configuration	CO118.3: Analyze and develop the characteristics of BJT and FET Circuits	L3
5. Measurement of pinch off voltage and plot transfer characteristics and drain characteristics of JFET.		
6. Transfer characteristics and drain characteristics of MOSFET.		
7. OP-AMP: Inverting and Non-Inverting Configuration. Record of Waveforms.	CO4: Implement Operational amplifier circuits.	L2
8. Half Wave and Full Wave Rectifier without Capacitor filter. Record of Waveforms, Measurement of Average and RMS value.		
9. Verification of Truth table of Logic gates (AND, OR, NOT, NAND, NOR, EX-OR)	CO118.5: Acquire knowledge on basic digital logic gates.	
10. Implementation of digital circuit using Universal gates.		



# SYNERGY INSTITUTE OF TECHNOLOGY, BHUBANESWAR

Approved by AICTE, New Delhi, Affiliated to BPUT, Odisha

## Department of Computer Science and Engineering

**Subject: Communicative English & Report Writing Lab.**

**Year/Semester: 1<sup>st</sup>/ 2<sup>nd</sup>**

**Subject Code: C119**

Module	CO Statement After completion of the course the student will be able to	BTL
<b>MODULE I:</b> -Self-Introduction, Reading Practice. -Listening Practice	<b>CO109.1:</b> Develop different listening & reading skills. Introduce oneself impact fully and effectively in a gathering.	L6
	<b>CO109.2:</b> Develop different listening skills.	L6
<b>MODULE II:</b> Professional and PowerPoint presentation, Review of a book, newspaper in editorial, movie.	<b>CO109.3:</b> Demonstrate effective oral presentations by interpreting and analyzing data.	L4
<b>MODULE III:</b> Situational conversation practice and role play.	<b>CO109.4:</b> Improve and practice spoken English in various hierarchical setups with varied goals.	L6
<b>MODULE IV:</b> Cover letter and CV writing.	<b>CO109.5:</b> Able to showcase strengths, achievements and experience as per the requirement of the job and improve employability.	L4
<b>MODULE V:</b> Group discussion and Mock Interview	<b>CO109.6:</b> Acquire strategic competence to use both spoken and written language in a range of wide communication strategies.	L6



# SYNERGY INSTITUTE OF TECHNOLOGY, BHUBANESWAR

Approved by AICTE, New Delhi, Affiliated to BPUT, Odisha

## Department of Computer Science and Engineering

SYNERGY-BHUBANESWAR

**Subject: Workshop & Digital Manufacturing Lab.**

**Year/Semester: 1<sup>st</sup>/ 2<sup>nd</sup>**

**Subject Code: C120**

Experiment Name	CO Statement After completion of the course the student will be able to	BTL
Preparation of job in fitting section/Study of lathe and turning operation. 6	<b>CO102.1</b> Learn different lathe operation and various types of fittings	<b>L1,L2,L6</b>
Preparation of job in black smith section/ Study of milling machine and milling operation.		
Preparation of job in carpentry section/milling operation on CNC milling machine	<b>CO102.2</b> Prepare jobs in black smith and to learn milling and turning operation in CNC lathe.	<b>L1,L2</b>
Study of CNC lathe machine and turning on CNC lathe		
Study of Robot (Pick and place and palletizing operation).	<b>CO102.3</b> Study basic concepts and movement of Robots.	<b>L1</b>
Study of additive manufacturing using 3D printer and product development.	<b>CO102.4-</b> Study manufacturing and product development in 3D printing	<b>L2</b>



# SYNERGY INSTITUTE OF TECHNOLOGY, BHUBANESWAR

Approved by AICTE, New Delhi, Affiliated to BPUT, Odisha

## Department of Computer Science and Engineering

**Subject: Mathematics-III**

**Year/Semester: 2<sup>nd</sup> / 3<sup>rd</sup>**

**Subject Code: C201**

Module	CO Statement After completion of the course the student will be able to	BTL
<b>MODULE 1: CONCEPTS:</b> Solution of Non-linear equation in one variable (Bisection, Secant, Newton Rapson Method, Fixed Point Iteration method). Numerical Solutions of system of Linear equations (Gauss-Seidel, Successive Over Relaxation, Doolittle method, Crouts method, Choleskys Method). Interpolation: Newton's forward and backward interpolation, Newton divided difference interpolation, Lagrange Interpolation.	<b>CO201.1-</b> Analyze typical approximate roots.  <b>CO201.2-</b> Enrich Fundamental Concept to approximate Function.	L4  L2
<b>MODULE 2:</b> Numerical Differentiation, integration and Solution of Differential Equations: Numerical Differentiation, The trapezoidal rule, The Simpson's rule, Gauss Integration formulas. Solution of ordinary differential equation: Euler's method, Improvement of Euler's method, Runge-Kutta methods, multi step methods, Methods for system and higher order ordinary differential equations.	<b>CO201.3-</b> Understand various methods relating to numerical values of Differentiation and integration.	L2
<b>MODULE 3:</b> Sample Space, Probability, Conditional Probability, Independent Events, Bayes' Theorem, Random variables, Probability distributions, Expectations, Mean and variance, Moments.	<b>CO201.4-</b> Analyze the Probability of events.	L4
<b>MODULE 4:</b> Bemoulli Trials, Binomial, Poisson, Hyper Geometric Distribution, Uniform.. Exponential and Normal distribution, Bivariaie Distributions.	<b>CO201.5-</b> Assess different real valued function linked with Probability function.	L5
<b>MODULE 5:</b> Correlation and Regression Analysis, Rank Correlation, Maximum Likely hood estimate, Method of Moments, Confidence intervals mean and variance of a Normal Distribution, p-value. Testing of hypothesis: test for goodness of fit, Test for single mean and variance of a Normal Distribution	<b>CO201.6-</b> Analyze co-relation and regression values in details Manner.	L4



# SYNERGY INSTITUTE OF TECHNOLOGY, BHUBANESWAR

Approved by AICTE, New Delhi, Affiliated to BPUT, Odisha

## Department of Computer Science and Engineering

**Subject: OOP Using JAVA**

**Year/Semester: 2<sup>nd</sup> / 3<sup>rd</sup>**

**Subject Code: C202**

Module	CO Statement After completion of the course the student will be able to	BTL
Module 1: An introduction to Programming. Different types of programming languages, Description of Compiler and Interpreter. Features of OOP, JVM, Tokens, Operators, Array, Scanner class	CO202.1 - Understand the basics of object-oriented programming using C++ and JAVA.	L2
Module 2: Classes, Methods, Data hiding, encapsulation, Constructor, Inheritance, wrapper Class, Dynamic method dispatching	CO202.2- Apply the concept of classes, Java, JDK Components and develop Simple Java Programs	L3
Module 3: Data Abstraction, Interface, Package, Exception Handling, Multithreading	CO202.3- Develop Simple Java Programs using inheritance and Exception handling	L4
Module 4: IO Streams, Applet	CO202.4- Develop web applications and can be executed by browsers for many platforms.	L4
Module 5: Swing, JLabel, JButton, JTextfield, JTextarea	CO202.5- Develop GUI applications using Swing components and Event handling programs.	L4
Module 6: Java FX, Create animation and Media	CO202.6- Develop the animation and media	L4



# SYNERGY INSTITUTE OF TECHNOLOGY, BHUBANESWAR

Approved by AICTE, New Delhi, Affiliated to BPUT, Odisha

## Department of Computer Science and Engineering

**Subject: Organizational Behaviour**

**Year/Semester: 2<sup>nd</sup> / 3<sup>rd</sup>**

**Subject Code: C203**

Module	CO Statement After completion of the course the student will be able to	BTL
<b>Module 1: Fundamentals Of OB</b> Definition, scope and importance of OB, Relationship between OB and the individual Evolution of OB, Theoretical framework (cognitive), behaviorist and social cognitive) Limitations of OB.	<b>CO203.1</b> -Learn about the Evolution of Organizational Behaviour and understand the managerial implications of different theories of Organizational Behaviour.	L1,L2
<b>Module 2 Attitude:</b> Importance of attitude in an organization, Components of attitude Right Attitude, Relationship between behavior and attitude, Developing Emotional intelligence at the workplace Job attitude, Barriers to changing attitudes. <b>Personality and values:</b> Definition and importance of Personality for performance The Myers Briggs Type Indicator and The Big Five personality model, Significant personality traits suitable to the workplace Personality Tests and their practical applications. Perception: Meaning and concept of perception, Factors influencing perception Perceptual process, Selective perception Social perception, Attribution theory stereotyping and halo effect. Motivation: Definition & Concept of Motive & Motivation, The Content Theories of Motivation (Maslow's Need Hierarchy & Herzberg's Two Factor model Theory) The Process Theories (Vroom's expectancy Theory & Porter Lawler model), Contemporary Theories — Equity Theory of Work Motivation.	<b>CO203.2</b> - Understand the importance of developing the Right Personality, Attitude, Perception to help them sustain in the workplace. Learn Motivation Theories /Models to improve Organizational Performance.	L3,L4
<b>Module 3:</b> Meaning of group and group behaviour, Group dynamics, Types of groups, The five stage model of group development. Managing Teams: Why Work Teams, Work Teams in Organization Developing Work Teams Team Effectiveness & Team Building Leadership: Concept of Leadership Styles of Leadership, Trait Approach Contingency Leadership Approach Meaning and significance of contemporary leadership- theories Concept of transformations leadership.	<b>CO203.3</b> -Understand the role of Group Behavior, ways to develop Effective Teams, understand various Leadership style, (practical applications role of leaders in decision making process)	L3
<b>Module 4: Organizational Culture :</b> Meaning& Definition of Organizational Culture Types of Culture, Creating & Sustaining Organizational Culture, Creating Positive	<b>CO203.4</b> -Develop Learning Attitude in workplace. Learn to adopt to new organizational culture and develop effective communication skills to resolve	L6



# SYNERGY INSTITUTE OF TECHNOLOGY, BHUBANESWAR

Approved by AICTE, New Delhi, Affiliated to BPUT, Odisha

## Department of Computer Science and Engineering

SYNERGY-BHUBANESWAR

Organizational Culture,. Concept of Workplace Spirituality.	workplace conflicts & stress.	
<b>Module 5: Organizational Change</b> Meaning, Definition, Nature, Importance , Types of Organizational Change, Implementing Organizational Change	CO203.5- Develop a life- long learning attitude to adopt to organizational change process and proactively implement the changes to sustain in the highly competitive business environment .	L6,L3
<b>Module 6: Approaches to managing Organizational Change</b> (Kurt Lewin's-Three step model, Seven Stage model of Change &Kotter's Eight-Step plan for Implementing Change) <b>Leading the Change Process</b> , Facilitating Change, Dealing with Individual &Group Resistance Intervention Strategies for Facilitating Organizational Change, Developing a Learning Organization.	CO203.6-Analyze the different Organizational Change Models and Change Intervention Methods to solve organizational problems.	L4,L3



# SYNERGY INSTITUTE OF TECHNOLOGY, BHUBANESWAR

Approved by AICTE, New Delhi, Affiliated to BPUT, Odisha

## Department of Computer Science and Engineering

**Subject: Digital Logic Design**

**Year/Semester: 2<sup>nd</sup> / 3<sup>rd</sup>**

**Subject Code: C204**

Module	CO Statement After completion of the course the student will be able to	BTL
Logic design, Transistor as switches, C-MOS gates, Sequential circuits, some examples. Digital systems- Representation of numbers, Binary codes, grey code, error detecting & error correcting codes, register, binary logics, basic logic gates Boolean Algebra- Boolean operations, Boolean functions, Algebraic manipulation, minterm & maxterms, sum of products and product of sum representation, two input logic gates, functional completeness	<b>CO204.1:</b> Study different Transistor switches, digital logic circuit, number system and different codes. <b>CO204.2:</b> Study different Boolean Functions and Boolean operations.	<b>L2</b>  <b>L2</b>
Minimization of Boolean functions- K- map, don't care conditions, prime implicants, Quine Mc-Cluskey technique, logic gates, NAND/NOR Gates, Universal gates	<b>CO204.3:</b> Describe the minimization tool to minimize the logical functions	<b>L4</b>
Combinational circuits- Adder, subtractor, multiplier, comparator, decoder, encoder, multiplexer, demultiplexers, MUX realization of switching functions, parity bit generator, code converters, Hazard & Hazard free realizations	<b>CO204.4:</b> Understand the working mechanism and design of different combinational circuit.	<b>L2</b>
Synchronous sequential circuits- Finite state machines, latches & flip-flops(SR,D, JK,T), synthesis of clocked sequence circuits, Steps in synchronous sequential circuits design, Design of Modulo- N Ring & Shift counters, serial binary adders.	<b>CO204.5:</b> Analyze the working mechanism and design of different sequential circuits.	<b>L4</b>
Registers & counters- Registers and shift registers, Sequential adders, Binary &BCD Ripple Counters, Synchronous counters Algorithmic State Machines- Salient features of ASM chart simple examples system design using Data path & control sub systems control implementations examples of weighing machine & binary multiplier.	<b>CO204.6:</b> Understand the basic idea of different counters, shift registers and algorithm of state machine.	<b>L2</b>



# SYNERGY INSTITUTE OF TECHNOLOGY, BHUBANESWAR

Approved by AICTE, New Delhi, Affiliated to BPUT, Odisha

## Department of Computer Science and Engineering

SYNERGY-BHUBANESWAR

**Subject: Data Structure**

**Year/Semester: 2<sup>nd</sup> / 3<sup>rd</sup>**

**Subject Code: C205**

Module	CO Statement After completion of the course the student will be able to	BTL
<b>Module I:</b> Basic terminologies: Elementary Data organizations, Data structure Operations: insertion, deletion, traversal etc.; Analysis of an Algorithm, Asymptotic Notations, and Time-Space trade off. Searching: Linear Search and Binary Search Techniques and their complexity analysis.	<b>CO205.1:</b> Learn the basic types for data structure, implementation and application.	<b>L3</b>
<b>Module II:</b> Stacks and Queues: ADT Stack and its operations: Algorithms and their complexity analysis. Applications of Stacks: Expression Conversion and evaluation corresponding algorithms and complexity analysis, ADT queue. Types of Queue: Simple Queue, Circular Queue, Priority Queue; Operations on each types of Queues: Algorithms and their analysis.	<b>CO205.2:</b> Know the strength and weakness of different data structures.	<b>L2</b>
<b>Module III:</b> Linked Lists: Singly linked lists: Representation in memory, Algorithms of several operations: Traversing, Searching. Insertion into, Deletion from linked list, Linked representation of Stack and Queue, Header nodes, Doubly linked list: operations on it and algorithmic analysis; Circular Linked Lists all operations their algorithms and the complexity analysis	<b>CO205.3:</b> Use the appropriate data structure in context of solution of given problem.	<b>L3</b>
<b>Module IV:</b> Sorting and Hashing: Objective and properties of different sorting algorithms: Selection Sort, Bubble Sort, Insertion Sort, Quick Sort, Merge Sort, Heap Sort; Performance and Comparison among all the methods, Hashing.	<b>CO205.4:</b> Develop programming skills which require solving given problem.  <b>CO205.5:</b> Understand different tree and graph operation	<b>L6</b>  <b>L2</b>
<b>Module V:</b> Trees: Basic Tree Terminologies, Different types of Trees: Binary Tree, Threaded Binary Tree. Binary L2Search Tree, AVL Tree: Tree operations on each of the trees and their algorithms with complexity analysis. Applications of Binary Trees. B Tree, B+ Tree: definitions, algorithms and analysis. Graph: Basic Terminologies and Representations, Graph search and traversal algorithms and complexity analysis.	<b>CO205.6:</b> Understand the complexity analysis of tree & Graph	<b>L2</b>



# SYNERGY INSTITUTE OF TECHNOLOGY, BHUBANESWAR

Approved by AICTE, New Delhi, Affiliated to BPUT, Odisha

## Department of Computer Science and Engineering

SYNERGY-BHUBANESWAR

**Subject: DIGITAL LOGIC DESIGN LAB.**

**Year/Semester: 2<sup>nd</sup> / 3<sup>rd</sup>**

**Subject Code: C207**

Experiment Name	CO Statement After completion of the course the student will be able to	BTL
Digital Logic Gates: Investigate logic behavior of AND, OR, NAND, NOR, EX-OR, EX-NOR, Invert and Buffer gates, use of Universal NAND Gate	<b>CO207.1:</b> Acquire knowledge on basic digital logic gates.	<b>L2</b>
Gate-level minimization: Two level and multi level implementation of Boolean functions.	<b>CO207.2:</b> Gate level minimization using universal gate	<b>L2</b>
Design, implement and test a given design example with (i) NAND Gates only (ii) NOR Gates only and (iii) using minimum number of Gates.		
Combinational Circuits: design, assemble and test: adders and subtractors, code converters, gray code to binary and 7 segment display	<b>CO207.3:</b> Design of combinational circuits and verify truth table	<b>L2</b>
Design with multiplexers and de-multiplexers.		
Flip-Flop: assemble, test and investigate operation of SR, D & J-K flip-flops.	<b>CO207.4:</b> Design of sequential circuits and verify truth table	<b>L2</b>
Shift Registers: Design and investigate the operation of all types of shift registers with parallel load.		
Counters: Design, assemble and test various ripple and synchronous counters - decimal counter, Binary counter with parallel load.		
Memory Unit: Investigate the behaviour of RAM unit and its storage capacity – 16 X 4 RAM: testing, simulating and memory expansion.	<b>CO207.5:</b> Test and simulate the memory unit and clock pulse generator	<b>L2</b>
Clock-pulse generator: design, implement and test.		



# SYNERGY INSTITUTE OF TECHNOLOGY, BHUBANESWAR

Approved by AICTE, New Delhi, Affiliated to BPUT, Odisha

## Department of Computer Science and Engineering

**Subject: Data Structure Lab.**

**Year/Semester: 2<sup>nd</sup> / 3<sup>rd</sup>**

**Subject Code: C208**

Experiment Name	CO Statement After completion of the course the student will be able to	BTL
1. Write a C Program to create a stack using an array and perform - i) Push operation, ii) Pop operation (b) Write a C Program to create a queue and perform - i) Push, ii) Pop, iii) Traversal 2. Write a C Program that uses functions to perform the following operations on a single linked list : i) Creation, ii) Insertion, iii) Deletion, iv) Traversal (b) Write a C Program that uses functions to perform the following operations on a double linked list: i) Creation, ii) Insertion, iii) Deletion 3. Write a C Program that uses functions to perform the following operations on a Binary Tree : i) Creation, ii) Insertion, iii) Deletion	<b>CO208.1:</b> Develop linear and non-linear data structure	<b>L6</b>
4 Write a C Program to construct an AVL-Tree and delete the selective nodes.	<b>CO208.2:</b> Express different operation on AVL tree	<b>L2</b>
5. Write a C Program that uses Stack Operations to perform the following:- i) Converting an infix expression into postfix expression ii) Evaluating the postfix expression	<b>CO208.3:</b> Evaluate infix to postfix expression	<b>L5</b>
6. C Programs on : i) Bubble sort, ii) Selection sort, iii) Insertion sort, iv) Quick sort, v) Radix sort vi) Heap sort, vii) 2 Way Merge Sort	<b>CO208.4:</b> Apply sorting algorithms in real life application	<b>L3</b>
7. C Programs on : i) Sequential Search, ii) Binary Search	<b>CO208.5:</b> Apply searching algorithms in real life application	<b>L3</b>



# SYNERGY INSTITUTE OF TECHNOLOGY, BHUBANESWAR

Approved by AICTE, New Delhi, Affiliated to BPUT, Odisha

## Department of Computer Science and Engineering

**Subject: OOP Using Java Lab.**

**Year/Semester: 2<sup>nd</sup> / 3<sup>rd</sup>**

**Subject Code: C209**

Experiment Name	CO Statement After completion of the course the student will be able to	BTL
1.Introduction ,Compiling, Executing a Java Program	<b>CO209.1-</b> Write programs for solving real world problems using java collection framework	<b>L2</b>
2.Classes and Objects, Data Abstraction & Data Hiding, Inheritance, Polymorphism	<b>CO209.2-</b> Write programs using abstract classes.,Classes having different forms	<b>L2</b>
3.Threads Program	<b>CO209.3-</b> Develop multiple flow of execution simultaneously	<b>L3</b>
4.Interfaces & Inner classes, Wrapper Classes	<b>CO209.4-</b> Write how can access through implementing various classes.	<b>L3</b>
5.Applet Programs	<b>CO209.5-</b> Write GUI programs using Applet	<b>L3</b>
6.Exception Handling Mechanism	<b>CO209.6-</b> Develop Exception handling mechanism.	<b>L5</b>



# SYNERGY INSTITUTE OF TECHNOLOGY, BHUBANESWAR

Approved by AICTE, New Delhi, Affiliated to BPUT, Odisha

## Department of Computer Science and Engineering

SYNERGY-BHUBANESWAR

**Subject: Evaluation of Internship-I**

**Year/Semester: 2<sup>nd</sup> / 3<sup>rd</sup>**

**Subject Code: C210**

	<b>CO Statement</b> <b>After completion of the course the student will be able to</b>	<b>BTL</b>
<b>CO210.1</b>	It focus on what students intend to accomplish for the internship organization during their work term.	L2
<b>CO210.2</b>	It describes what students will add to their knowledge, skills and development in that area.	L3
<b>CO210.3</b>	It describes the students punctuality at the place of internship how they will perform their duties.	L2
<b>CO210.4</b>	The activity is the expected achievement, within the time frame expected completion with date and the evaluation is the stated method of measurement.	L2
<b>CO210.5</b>	It gives a chance to gain real-world experience beyond classrooms..	L5



**Subject: Discrete Mathematics**

**Year/Semester: 2<sup>nd</sup> / 4<sup>th</sup>**

**Subject Code: C211**

**After completion of the course the student will be able**

### **Moduls -1**

Sets and propositions, Principle of inclusion and exclusion. Mathematical induction, propositions, Logical connectives Conditional and Biconditionals, Logical equivalences, Predicate Calculus, Quantifiers, Theory of inference, Methods of Proof.

### **Statement**

Co1 - To understand various methods for proof of propositions.

### **Module-2**

Relations and functions, Properties of binary relations, closure of relations. Warshall's algorithm Equivalence relations. Partial ordering relations and lattices, chains and antichains, functions, composite functions, Invertible functions, Recursive functions, Pigeonhole principle.

**CO2-** To study and analyse different relations, functions.

### **Module-3**

Numeric functions and Generating functions, Discrete numerical functions, Recurrence Relations, Recursive Algorithms. Recurrence Relations, Linear with constant coefficients. Solution by method of generating functions. Divide and conquer algorithm.

**Co3-** Emphasize fundamental concept to Solve Recurrence Relations

### **Module-4**

Groups, Rings, subgroups, cosets, Lagrange's theorem, Codes and group error detection, Connection using group character. Isomorphism, homomorphism, Normal subgroups. Ring, Integral domain, fields. Boolean algebra Lattice, Algebraic systems, Principle of duality, Distributive Complemented lattices. Boolean function expression, Simplification of logic expression using Karnaugh Map. Design, Implementation of digital networks, switching Circuits.

**Co4-** to analyse different groups and relations in groups.

### **Module-5-**

Graphs and Trees, Basic Terminology Directed graphs and subgraphs, Representation of graphs Operation on graphs, paths, Cycle Graph traversals, Shortest path in weighted graphs. Eulerian path Consists Hamiltonian path, Circuits, Traveling Salesman problem. Planar Graphs, Graph Colouring, The Four Colour theorem, Rooted trees, Binary Search tree, Spanning trees. Minimum spanning trees, Kruskal's Algorithm, Prim's Algorithm...

**Co5-** To analyse different graphs and relations in graphs.

**Co6-** To understand the fundamental concept of graph theory problems and solutions.



# SYNERGY INSTITUTE OF TECHNOLOGY, BHUBANESWAR

Approved by AICTE, New Delhi, Affiliated to BPUT, Odisha

## Department of Computer Science and Engineering

**Subject:** Design and Analysis of Algorithm

**Year/Semester:** 2<sup>nd</sup> / 4<sup>th</sup>

**Subject Code:** C212

Module	CO Statement After completion of the course the student will be able to	BTL
<b>Module 1: Introduction:</b> Characteristics of algorithm. Analysis of algorithm: Asymptotic analysis of complexity bounds – best, average and worst-case behavior; Performance measurements of Algorithm, Time and space trade-offs, Analysis of recursive algorithms through recurrence relations: Substitution method, Recursion tree method and Masters' theorem.:	<b>CO212.1-</b> Analyze different types of algorithms using different methods to calculate the best and worst-case running times of algorithms based on asymptotic analysis analyze the performance and justify the correctness of algorithms.	<b>L4</b>
<b>Module II:</b> Fundamental Algorithmic Strategies: Brute-Force: : Linear search, selection sort, Greedy: Huffman coding, Fractional knapsack problem, Activity selection Problem, Dynamic Programming: matrix chain multiplication, Longest common subsequence, Travelling Salesman Problem, Branch- and-Bound and Backtracking methodologies for the design of algorithms; Illustrations of these techniques for Problem-Solving , Bin Packing, Knap Sack TSP. Heuristics – characteristics and their application domains.	<b>CO21.2-</b> Describe the different paradigm and explain the algorithmic design situation, synthesize it and analyze it to determine the computational complexity.	<b>L5</b>
<b>Module III:</b> Graph and Tree Algorithms: Traversal algorithms: Depth First Search (DFS) and Breadth First Search (BFS); Shortest path algorithms, Transitive closure, Minimum Spanning Tree, Topological sorting, Network Flow Algorithm.	<b>CO212.3-</b> Solve the problems using different algorithms written for Trees and Graphs.	<b>L3</b>
<b>Module IV:</b> Tractable and Intractable Problems: Computability of Algorithms, Computability classes – P, NP, NP-complete and NP-hard. Cook's theorem, Standard NP-complete problems (Clique Decision, Node cover Decision and Chromatic Number Decision problem) and Reduction techniques.	<b>CO212.4-</b> Analyse and compute randomized algorithms and examine computational problems.	<b>L4</b>
<b>Module V:</b> Approximation algorithms: Node cover problem, Travelling sales man problem, Randomized algorithms: Quick sort, n-queen problem, Min cut, Class of problems beyond NP – P SPACE	<b>CO212.5-</b> Explain and Compute the approximation algorithm using different factors.	<b>L4</b>
	<b>CO212.6-</b> Explain and compute the Randomized algorithm using different factors	<b>L4</b>



# SYNERGY INSTITUTE OF TECHNOLOGY, BHUBANESWAR

Approved by AICTE, New Delhi, Affiliated to BPUT, Odisha

## Department of Computer Science and Engineering

SYNERGY-BHUBANESWAR

**Subject:** Engineering Economics

**Year/Semester:** 2<sup>nd</sup> / 4<sup>th</sup>

**Subject Code:** C213

Module	CO Statement After completion of the course the student will be able to	BTL
<p><b>MODULE 1: CONCEPTS</b> : Concept of Engineering Economics, Origin of EE, Definitions , Scope. Framework of Engineering Economics, Basic problems of an economy, Principles and Nature of Engineering Economics. Basic problems of an economy, Micro Economics and Macro Economics.</p> <p><b>Demand:</b> Meaning of demand, Nature of Demand, Types of Demand, Determinants of demand, Demand function, Demand Schedule and Demand Curve, Law of Demand, assumptions underlying the law of Demand and exceptions to the law of Demand. Elasticity of demand: concept of Elasticity of Demand, Types – Price ( types and Simple numerical problems to be solved) Income, Cross, and advertising Elasticity of Demand (Simple numerical problems to be solved ) Demand Forecasting – Meaning and techniques</p> <p><b>Supply:</b> Meaning of supply, Nature, Determinants of Supply, Supply Function Law of supply and its exception Elasticity of supply: Meaning, Degrees of Elasticity, and factors of Elasticity of Supply. <b>Determination of market equilibrium</b> (Simple numerical problems to be solved).</p>	<p><b>CO213.1</b>-Know about the Meaning and Relevance of Engineering Economics in Business Decision making – Concept of demand &amp; supply, Elasticity of demand and equilibrium price determination</p>	<p><b>L1,L2</b></p>
<p><b>Module 2 : Production</b> : Concept of Production and factors of Production, Production function Laws of returns: Law of variable proportion, Law of returns to scale <b>Cost and Revenue Concepts</b> : concept of Cost, Total Costs, Fixed cost, Variable cost <b>Revenue Concepts:</b> Total revenue, Average revenue and Marginal revenue <b>Cost-Output Relationships</b> in the Short Run and Long Run, Analysis of cost minimization</p>	<p><b>CO213.2</b>-Know about the laws of Production ,Cost Minimization Methods and Cost Output Relations in long Run &amp; Short Run to manufacture quality goods at the least cost combinations of Inputs</p>	<p><b>L4,L3</b></p>
<p><b>Module 3: Market</b> : Basic understanding of different market structures Determination of equilibrium price under perfect competition (Simple numerical problems to be solved), <b>Break Even Analysis:</b> linear approach (Simple numerical problems to be solved).</p>	<p><b>CO213.3</b>-Understand different market structure and its impact on price determination of goods. Understand the concept of Break Even Analysis with its effect on profitability of firms using mathematical formulas.</p>	<p><b>L4,L3</b></p>



# SYNERGY INSTITUTE OF TECHNOLOGY, BHUBANESWAR

Approved by AICTE, New Delhi, Affiliated to BPUT, Odisha

## Department of Computer Science and Engineering

SYNERGY-BHUBANESWAR

<b>Module 4: Time Value of Money:</b> Interest - Simple and compound, nominal and effective rate of interest, Cash flow diagrams, Principles of economic equivalence <b>Evaluation of Engineering Projects:</b> Present worth method, Future worth method, Annual worth method Internal rate of return method, Cost benefit analysis for public projects <b>Depreciation-:</b> Depreciation of capital asset, Causes of depreciation <b>Methods of calculating depreciation :</b> Straight line method, Declining balance method, SOYD method, After tax comparison of project.	<b>CO213.4-</b> Know about Time value of Money, cash flow problems, evaluate various engineering projects using engineering formulas and calculating Depreciation of Fixed Assets.	<b>L5</b>
<b>Module 5:Inflation:</b> Meaning of inflation, types, causes, measures to control inflation <b>National Income:</b> Definition, Concepts of national income, Method of measuring national income.	<b>CO213.5-</b> Learn about concepts of National Income, GNP, GDP, Inflation, Fiscal Policy and Macro economic variables to understand the working of National Economy as a whole.	<b>L2</b>
<b>Module 6: Banking :</b> Commercial bank, Functions of commercial bank, Central bank, Functions of Central Bank.	<b>CO213.6-</b> Develop Knowledge About Role of RBI, And Commercial Banks- (Monetary Policy)	<b>L2</b>



# SYNERGY INSTITUTE OF TECHNOLOGY, BHUBANESWAR

Approved by AICTE, New Delhi, Affiliated to BPUT, Odisha

## Department of Computer Science and Engineering

**Subject: Computer Organization and Architecture**

**Year/Semester: 2<sup>nd</sup> / 4<sup>th</sup>**

**Subject Code: C214**

Module	CO Statement After completion of the course the student will be able to	BTL
<b>Module-I:</b> Functional blocks of a computer: CPU, memory, input-output subsystems, control unit. Instruction set architecture of a CPU—registers, instruction execution cycle, RTL interpretation of instructions, addressing modes, instruction set.	<b>CO214.1</b> -Understand the basics of instructions sets and their impact on processor design	L2
<b>Module-II:</b> Data representation: signed number representation, fixed and floating point representations, character representation. Computer arithmetic – integer addition and subtraction, ripple carry adder, carry look-ahead adder, etc. multiplication – shift and add, Booth multiplier, carry save multiplier, etc. Division restoring and non-restoring techniques, floating point arithmetic.	<b>CO214.2</b> -Demonstrate an understanding of the design of the functional units of a digital computer system.	L3
<b>Module-III:</b> Introduction to x86 architecture. CPU control unit design: hardwired and micro-programmed design approaches, Memory system design: semiconductor memory technologies, memory organization. Peripheral devices and their characteristics: Input-output subsystems, I/O device interface, I/O transfers. Programs and processes	<b>CO214.3</b> -Evaluate cost performance and design trade-offs in designing and constructing a computer processor including memory.	L4
<b>Module-IV:</b> Pipelining: Basic concepts of pipelining, throughput and speedup, pipeline hazards. Parallel Processors: Introduction to parallel processors, CPU Basics: Multiple CPUs, Cores, and Hyper-Threading, Introduction to Multiple-Processor Scheduling in Operating System.	<b>CO214.4</b> -construct a computer processor including memory.	L6
	<b>CO214.5</b> -Design a pipeline for consistent execution of instructions with minimum hazards	L6
<b>Module-V:</b> Memory organization: Memory interleaving, concept of hierarchical memory organization, cache memory, cache size vs. block size, mapping functions, replacement algorithms, and write policies.	<b>CO214.6</b> -Manipulate representations of numbers stored in digital computers	L4



# SYNERGY INSTITUTE OF TECHNOLOGY, BHUBANESWAR

Approved by AICTE, New Delhi, Affiliated to BPUT, Odisha

## Department of Computer Science and Engineering

**Subject: Data Communication**

**Year/Semester: 2<sup>nd</sup> / 4<sup>th</sup>**

**Subject Code: C215**

Module	CO Statement After completion of the course the student will be able to	BTL
<b>Module I:</b> INTRODUCTION TO DATA COMMUNICATIONS AND NETWORKING: Standards Organizations for Data Communications, Layered Network Architecture, Open Systems Interconnection, Data Communications Circuits, Serial and parallel Data Transmission, Data communications Networks, Alternate Protocol Suites. <b>SIGNALS, NOISE, MODULATION, AND DEMODULATION:</b> Signal Analysis, Electrical Noise and Signal-to-Noise Ratio, Analog Modulation Systems, Information Capacity, Bits, Bit Rate, Baud, and M-ary Encoding, Digital Modulation.	<b>CO215.1-</b> Understand the data communication Network Architecture & concept of Signal Analysis, Modulation & Demodulation	<b>L2</b>
<b>Module II:</b> METALLIC CABLE TRANSMISSION MEDIA: Metallic Transmission Lines, Transverse Electromagnetic Waves, Characteristics of Electromagnetic Waves. <b>OPTICAL FIBER TRANSMISSION MEDIA:</b> Advantages of Optical Fiber cables, Disadvantages of Optical Fiber Cables, Electromagnetic spectrum, Optical Fiber Communications System Block Diagram, Optical Fiber construction, Propagation of Light Through an Optical fiber Cable, Optical Fiber Modes and Classifications, Optical Fiber Comparison, Losses in Optical Fiber Cables, Light sources, Light Detectors, Lasers.	<b>CO215.2-</b> Understand the concept & technique of Cable & Fiber Transmission Media.	<b>L2</b>
<b>Module III:</b> DIGITAL TRANSMISSION: Pulse Modulation, Pulse code Modulation, Dynamic Range, Signal Voltage to- Quantization Noise Voltage Ratio, Linear Versus Nonlinear PCM Codes, Companding, PCM Line Speed, Delta Modulation PCM and Differential PCM. <b>MULTIPLEXING AND T CARRIERS:</b> Time- Division Multiplexing, T1 Digital Carrier System, Digital Line Encoding, T Carrier systems, Frequency- Division Multiplexing, Wavelength- Division Multiplexing, Synchronous Optical Network.	<b>CO215.3-</b> Understand the concept of different type of modulation in data communication . <b>CO215.4-</b> Understand Multiplexing & T1 Digital Carrier System	<b>L2</b>
<b>Module IV:</b> WIRELESS COMMUNICATIONS SYSTEMS: Electromagnetic Polarization, Electromagnetic Radiation, Optical Properties of Radio Waves, Terrestrial Propagation of Electromagnetic Waves, Skip Distance, Free-Space Path Loss, Microwave Communications Systems, Satellite Communications Systems.	<b>CO215.5-</b> Understand the concept of wireless communications systems like Electromagnetic Polarization, Electromagnetic Radiation etc.	<b>L2</b>



# SYNERGY INSTITUTE OF TECHNOLOGY, BHUBANESWAR

Approved by AICTE, New Delhi, Affiliated to BPUT, Odisha

## Department of Computer Science and Engineering

SYNERGY.BHUBANESWAR

<b>Module V:</b> DATA COMMUNICATIONS CODES, ERROR CONTROL, AND DATA FORMATS: Data Communications Character Codes, Bar Codes, Error Control, Error Detection and Correction, Character Synchronization. DATA COMMUNICATIONS EQUIPMENT: Digital Service Unit and Channel Service Unit, Voice- Band Data Communication Modems, Bell Systems-Compatible Voice- Band Modems, Voice- Band Modem Block Diagram, Voice- Band Modem Classifications, Asynchronous Voice-Band Modems, Synchronous Voice-Band Modems, Modem Synchronization, 56K Modems, Modem Control: The AT Command Set, Cable Modems.	<b>CO215.6-</b> Understand the concept of data communications codes & error control	<b>L2</b>
---------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------	-------------------------------------------------------------------------------------	-----------



# SYNERGY INSTITUTE OF TECHNOLOGY, BHUBANESWAR

Approved by AICTE, New Delhi, Affiliated to BPUT, Odisha

## Department of Computer Science and Engineering

**Subject: Microprocessor and Microcontroller**

**Year/Semester: 2<sup>nd</sup> / 4<sup>th</sup>**

**Subject Code: C216**

Module	CO Statement After completion of the course the student will be able to	BTL
<b>Module-1</b> Introduction to 8 bit and 16 bit Microprocessors-H/W architecture Introduction to microprocessor, computer and its organization, Programming system; Address bus, data bus and control bus, Tristate bus; clock generation; Connecting Microprocessor to I/O devices; Data transfer schemes; Architectural advancements of microprocessors. Introductory System design using microprocessors; 8086 – Hardware Architecture; External memory addressing; Bus cycles; some important Companion Chips; Maximum mode bus cycle; 8086 system configuration; Memory Interfacing; Minimum mode system configuration, Interrupt processing.	<b>CO216.1:</b> Understand the basic architecture of 8085 and 8086.	L3
<b>Module 2</b> -16-bit microprocessor instruction set and assembly language programming; Programmer's model of 8086; operand types, operand addressing; assembler directives, instruction Set-Data transfer group, Arithmetic group, Logical group.	<b>CO216.2:</b> Impart the knowledge about the instruction set of 8086 and programing.	L4
<b>Module 3</b> -Microprocessor peripheral interfacing: Introduction; Generation of I/O ports; Programmable Peripheral Interface (PPI)-Intel 8255; Sample-and-Hold Circuit and Multiplexer; Keyboard and Display Interface; Keyboard and Display Controller (8279).	<b>CO216.3:</b> Understand the basic idea about the peripheral interfacing like 8255, 8251, and 8279.	L2
<b>Module 4</b> -8-bit microcontroller- H/W architecture instruction set and programming: Introduction to 8051 Micro-Controllers, Architecture; Memory Organization; Special Function register; Port Operation; Memory Interfacing, I/O Interfacing; Programming 8051	<b>CO216.4:</b> Understand the basic architecture of for INTEL 8051 architecture instruction set.	L3
<b>Module 5</b> -Resources, interrupts; Programmer's model of 8051; Operand types, Operand addressing; Data transfer instructions, Arithmetic instructions, Logic instructions, Control transfer instructions; Programming	<b>CO216.5:</b> Develop skill in simple program writing.	L2
<b>Module 6</b> -8086: Maximum mode system configuration, Direct memory access, Interfacing of D-to-A converter, A-to-D converter, CRT Terminal Interface, Printer Interface, Programming of 8051 timers, 8051 serial interface, Introduction to 80386 and 80486 Microprocessor family.	<b>CO216.6:</b> Understand the basic architecture of 80386 and 80486.	L3



# SYNERGY INSTITUTE OF TECHNOLOGY, BHUBANESWAR

Approved by AICTE, New Delhi, Affiliated to BPUT, Odisha

## Department of Computer Science and Engineering

**Subject: Digital Signal Processing**

**Year/Semester: 2<sup>nd</sup> / 4<sup>th</sup>**

**Subject Code: C217**

Module	CO Statement After completion of the course the student will be able to	BTL
1. Discrete Time System: Basic Discrete Time Signals and their classifications, Discrete time systems and their classifications, Stability of discrete time system, Analysis and response (convolution sum) of discrete - time linear LTI system, Recursive and Non-recursive discrete time system, impulse response of LTI system, Correlation of discrete time Signal.	<b>CO217.1:</b> Understand the characteristics of different digital systems like linear time-invariant systems and others	<b>L2</b>
2. Z-Transform and Its Application to the Analysis of LTI Systems: Z-Transform, Direct Z-Transform, properties of the Z- Transform, Inverse Z-Transform, Inversion Z-Transform by Power Series Expansion, Inversion of the Z-Transform by Partial-Fraction Expansion, Analysis of Linear Time-Invariant Systems in the z-Domain.	<b>CO217.2:</b> Explain z-transform to analyze the operations on signals and acquire knowledge about Systems	<b>L3</b>
3. Discrete Fourier Transform: Frequency-Domain Sampling and Reconstruction of Discrete-Time Signals, Discrete Fourier Transform, DFT as a Linear Transformation, Relationship of DFT to other Transforms, Properties of DFT: Periodicity, Linearity, and Symmetry Properties, Multiplication of Two DFTs and Circular Convolution, Use of DFT in Linear Filtering, Filtering of Long Data Sequences	<b>CO217.3:</b> Apply different signal Processing algorithms like DFT and FFT to the different signals.	<b>L3</b>
4. Structure and Implementation of FIR and IIR Filter: Structure for the Realization of Discrete-Time Systems, Structure of FIR Systems: Direct- Form Structure, Cascade-Form Structure, Frequency-Sampling Structure	<b>CO217.4:</b> Analyze the different structure of FIR and IIR filter.	<b>L4</b>
5. Design of FIR Filters: Symmetric and Antisymmetric FIR Filters, Design of Linear-Phase FIR Filters by using Windows, Design of Linear-Phase FIR Filters by Frequency-Sampling Method. Structure for IIR Systems: Direct-Form Structure, Signal Flow Graphs and Transposed Structure, Cascade-Form Structure, Parallel-Form Structure. Design of IIR Filters from	<b>CO217.5:</b> Design of different FIR filters	<b>L2</b>
6. Analog Filters: IIR Filter Design by Impulse Invariance, IIR Filter Design by the Bilinear Transformation. Basic adaptive filter: Structure of Adaptive FIR filter, System Modeling and Inverse Modeling, Matlab realization of DFT, FFT, Z-transform, IIR, FIR and adaptive filter.	<b>CO217.6:</b> Design the IIR filter and to develop Matlab code for DFT, FFT, IIR, and FIR	<b>L2</b>



# SYNERGY INSTITUTE OF TECHNOLOGY, BHUBANESWAR

Approved by AICTE, New Delhi, Affiliated to BPUT, Odisha

## Department of Computer Science and Engineering

**Subject: Constitution of India**

**Year/Semester: 2<sup>nd</sup> / 4<sup>th</sup>**

**Subject Code: C218**

Module	CO Statement After completion of the course the student will be able to	BTL
<b>MODULE 1:</b> Meaning of Constitutional Law and Constitutionalism Historical Perspective of Constitution of India, Salient Features and Characteristics of Constitution of India	<b>CO1:</b> Know about the Concept of Constitution and Importance of Constitutional Laws in governing a country. Learn about the evolution of Constitution of India with special reference to Constituent Assembly. Learn about the Salient features of Constitution of India	<b>L2</b>
Scheme of the Fundamental Rights Scheme of the Fundamental Right to Equality Scheme of the Fundamental Right to Certain Freedom under Article 19 Scope of Right to Life and Personal Liberty under Article 21	<b>CO2:</b> Understand of the Fundamental Rights and its Importance in people lives	<b>L2</b>
The scheme of Fundamental Duties and its Legal Status The Directive Principles of State Policy – Its Importance and Implementation	<b>CO3:</b> Learn about the 11 fundamental Duties and its importance in ruling the people of India . Understand the concept and significance of Directive Principles of State Policy	<b>L2</b>
Federal Structure and Distribution of Legislative and Financial Powers between the Union and the States Parliamentary Form of Government in India – The Constitutional Powers and Status of President of India Local Self Government- Constitutional Scheme in India	<b>CO4:</b> Understand the features of Federal and Unitary Structure of Indian Government Machinery Knowledge about the features of Indian Parliamentary system and its functions and Powers. Knowledge about the roles and powers of President of India Learn about the Panchayati Raj System in India and Provincial Autonomy	
Amendment of the Constitutional Powers and Procedure The Historical Perspectives of the Constitutional Amendments in India	<b>CO5:</b> Learn about the Importance of Constitutional Amendments and the various Amendments made in Constitution over the years	<b>L2</b>
Emergency Provisions: National Emergency, President Rule, Financial Emergency	<b>CO6:</b> Know about the Emergency situation in India and the different types of Emergency along with its provisions and Special powers of Central Government during Emergency	<b>L2</b>



# SYNERGY INSTITUTE OF TECHNOLOGY, BHUBANESWAR

Approved by AICTE, New Delhi, Affiliated to BPUT, Odisha

## Department of Computer Science and Engineering

**Subject: Problem Solving and Python Programming Lab.**

**Year/Semester: 2<sup>nd</sup> / 4<sup>th</sup>**

**Subject Code: C219**

Experiment Name	CO Statement After completion of the course the student will be able to	BTL
1. Compute the GCD of two numbers.	<b>CO1</b> -Interpret the fundamental Python syntax and semantics and be fluent in the use of Python control flow statements.	<b>L2</b>
2. Find the square root of a number (Newton's method) 3. Exponentiation (power of a number) 4. Find the maximum of a list of numbers	<b>CO2</b> -Implement Conditionals and Loops for Python Programs	<b>L3</b>
5. Linear search and Binary search 6. Selection sort, Insertion sort 7. Merge sort 8. First n prime numbers 9. Multiply matrices	<b>CO3</b> - Express proficiency in the handling of strings and functions	<b>L2</b>
10. Programs that take command line arguments (word count)	<b>CO4</b> -Use functions and represent Compound data using Lists, Tuples and Dictionaries	<b>L3</b>
11. Find the most frequent words in a text read from a file 12. Simulate elliptical orbits in Pygame 13. Simulate bouncing ball using Pygame	<b>CO-5</b> Read and write data from & to files in Python and develop Application using Pygame	<b>L3</b>



# SYNERGY INSTITUTE OF TECHNOLOGY, BHUBANESWAR

Approved by AICTE, New Delhi, Affiliated to BPUT, Odisha

## Department of Computer Science and Engineering

**Subject: Design and Analysis of Algorithm Lab**

**Year/Semester: 2<sup>nd</sup> / 4<sup>th</sup>**

**Subject Code: C220**

Experiment Name	CO Statement After completion of the course the student will be able to	BTL
1.Using a stack of characters, convert an infix string to postfix string. 2.Implement insertion, deletion, searching of a BST.	<b>CO1-</b> Implement different operation and function of BST using C	L3
3.Implement binary search and linear search in a program 4.Implement a heap sort using a max heap 5.Write a program on Quick sort algorithm. 6.Write a program on merge sort algorithm.	<b>CO2-</b> Implement different searching and sorting techniques using C	L3
7.Implement DFS/ BFS for a connected graph. 8.Implement Dijkstra's shortest path algorithm using BFS.	<b>CO3-</b> Implement different shortest path algorithm using C	L3
9.Write a program to implement Huffman's algorithm. 10.Implement MST using Kruskal /Prim algorithm	<b>CO4-</b> Construct minimum spanning tree using different techniques using C	L3
11.Using dynamic programming implement LCS	<b>CO5-</b> Implement dynamic programming problem using different techniques using C	L3
12.Find out the solution to the N-Queen problem. 13.Implement back tracking using game trees.	<b>CO6-</b> Implement N-queen problem using C	L3



# SYNERGY INSTITUTE OF TECHNOLOGY, BHUBANESWAR

Approved by AICTE, New Delhi, Affiliated to BPUT, Odisha

## Department of Computer Science and Engineering

**Subject: Computer Organization & Architecture Lab**

**Year/Semester: 2<sup>nd</sup> / 4<sup>th</sup>**

**Subject Code: C221**

Experiment Name	CO Statement After completion of the course the student will be able to	BTL
Demonstrate the several functional units of a PC physically.	<b>CO1-</b> Understand the primary functional units of a PC and their respective roles.	<b>L2</b>
Assemble a PC, Dismantle a PC	<b>CO2-</b> Understand designing, assembling, and disassembling a PC involves selecting compatible components based on user needs and system requirements, carefully connecting and disconnecting parts	<b>L2</b>
Study of SMPS	<b>CO3-</b> Gain an understanding of efficient power conversion techniques, including the principles of regulating voltage, reducing energy loss, and enhancing the performance and reliability of electronic devices.	<b>L2</b>
Understand different byte addressing instruction using 8085 simulator	<b>CO-4</b> Understand different byte addressing instructions using an 8085 simulator, students gain practical knowledge of how to manipulate memory and data effectively, enhancing their skills in low-level programming and microprocessor operations.	<b>L2</b>
Design of multiplexer and demultiplexer using VHDL	<b>CO-5</b> Design multiplexers and demultiplexers, and gain a comprehensive understanding of data routing, signal selection, and distribution in digital circuits, which are fundamental concepts in digital electronics and communication systems.	<b>L2</b>
C++ implementation of Booth's algorithm	<b>CO6-</b> By implementing Booth's algorithm in C++, students learn efficient techniques for binary multiplication, enhancing their understanding of arithmetic operations, algorithm optimization, and practical applications in computer engineering	<b>L3</b>



# SYNERGY INSTITUTE OF TECHNOLOGY, BHUBANESWAR

Approved by AICTE, New Delhi, Affiliated to BPUT, Odisha

## Department of Computer Science and Engineering

**Subject: Formal Language and Automata Theory**

**Year/Semester: 3<sup>rd</sup> / 5<sup>th</sup>**

**Subject Code: C301**

Experiment Name	CO Statement After completion of the course the student will be able to	BTL
<b>Module I:</b> Introduction: Alphabets, Strings and Languages; Automata and Grammars, Deterministic finite Automata (DFA)-Formal Definition, Simplified notation: State transition graph, Transition table, Language of DFA, Nondeterministic finite Automata (NFA), Language of NFA, Equivalence of NFA and DFA, Minimization of Finite Automata	<b>CO1:</b> Determine the practical problem to Language.	<b>L3</b>
	<b>CO2:</b> Design and determine the Automata	<b>L3</b>
<b>Module II:</b> Regular Expression (RE): Definition, Operators of regular expression and their precedence, Algebraic laws for Regular expressions, Kleen's Theorem, Regular expression to FA, DFA to Regular expression, Arden Theorem, Non Regular Languages, Decision properties of Regular Languages, FA with output: Moore and Mealy machine, Equivalence of Moore and Mealy Machine, Applications and Limitation of FA	<b>CO3:</b> Analyze the Regular Expressions using different Theorem	<b>L4</b>
<b>Module III:</b> Context Free Grammar (CFG) and Context Free Languages (CFL): Definition, Ambiguous to Unambiguous CFG, Useless symbols, Simplification of CFGs, Normal forms for CFGs: CNF and GNF, Closure proper ties of CFLs, Decision Properties of CFL	<b>CO4:</b> Demonstrate an increased level of Mathematical sophistication	<b>L3</b>
<b>Module IV:</b> Push Down Automata (PDA): Description and definition, Language of PDA, Acceptance by Final state, Acceptance by empty stack, Deterministic PDA, Equivalence of PDA and CFG, CFG to PDA	<b>CO5:</b> Derive And Simplify Context free grammar and Context free Language	<b>L3</b>
<b>Module V:</b> Turing machines (TM): Basic model, Language acceptance by TM, Variants of Turing Machine, Universal TM, Church's Thesis, Recursive and recursively enumerable languages, Halting problem,	<b>CO6:</b> Apply Mathematical and Formal techniques for solving problems	<b>L4</b>



# SYNERGY INSTITUTE OF TECHNOLOGY, BHUBANESWAR

Approved by AICTE, New Delhi, Affiliated to BPUT, Odisha

## Department of Computer Science and Engineering

SYNERGY.BHUBANESWAR

Introduction to Undesirability, Undecidable problems about TMs.PCP		
-----------------------------------------------------------------------	--	--



# SYNERGY INSTITUTE OF TECHNOLOGY, BHUBANESWAR

Approved by AICTE, New Delhi, Affiliated to BPUT, Odisha

## Department of Computer Science and Engineering

**Subject: Data Base Management System**

**Year/Semester: 3<sup>rd</sup> / 5<sup>th</sup>**

**Subject Code: C302**

Module	CO Statement After completion of the course the student will be able to	BTL
<b>Module I:</b> Introduction: Purpose of Database System -- Views of data – data models, database management system, three-schema architecture of DBMS, components of DBMS. E/R Model - Conceptual data modelling - motivation, entities, entity types, attributes relationships, relationship types, E/R diagram notation, examples.	<b>CO1-</b> Analyze the different database design models for various practical data base applications	<b>L4</b>
<b>Module II:</b> Relational Model: Relational Data Model - Concept of relations, schema-instance distinction, keys, relational+ algebra operators, SQL - Introduction, data definition in SQL, table, key and foreign key definitions, update behaviours. Querying in SQL, notion of	<b>CO2-</b> Develop SQL queries according to the various needs of the users and to optimize it for a specific application.	<b>L6</b>
<b>Module III:</b> Database Design: Dependencies and Normal forms, dependency theory - functional dependencies, Armstrong's axioms for FD's, closure of a set of FD's, minimal covers, definitions of 1NF, 2NF, 3NF and BCNF, normalization, 4NF, and 5NF	<b>CO3-</b> Design and develop the database with optimal query processing and normalization and decomposition techniques	<b>L6</b>
<b>Module IV:</b> Transactions: Transaction processing and Error recovery - concepts of transaction processing, ACID properties, concurrency control, locking based protocols for CC, error recovery and logging	<b>CO4-</b> Apply concurrency control & recovery mechanism for practical problems in database design	<b>L3</b>
<b>Module V:</b> Implementation Techniques: Data Storage and Indexes - file organizations, primary, secondary index structures, various index structures - hash-based, dynamic hashing techniques, multi-level indexes, B+ trees.	<b>CO5-</b> Identify the different advanced database techniques and apply it	<b>L3</b>
	<b>CO6-</b> Secure their database with the help of various security mechanisms	<b>L3</b>

**Subject: Operating System**



# SYNERGY INSTITUTE OF TECHNOLOGY, BHUBANESWAR

Approved by AICTE, New Delhi, Affiliated to BPUT, Odisha

## Department of Computer Science and Engineering

Year/Semester: 3<sup>rd</sup> / 5<sup>th</sup>

Subject Code: C303

Module	CO Statement After completion of the course the student will be able to	BTL
<b>Module I:</b> Operating Systems –Definition, Types, Functions , Abstract view of OS. System Structures –System Calls, Virtual Machines, Process Concepts –Threads , Multithreading.	<b>CO1-</b> Understand the basic concepts and functions of operating systems.	L2
<b>Module II:</b> Process Scheduling- Process Co-ordination –Synchronization ,Semaphores , Monitors Hardware Synchronization , Deadlocks –Methods for Handling Deadlocks	<b>CO2-</b> Understand the concept of Processes Scheduling,	L3
	<b>CO3-</b> Analyze the Threads and Deadlocks Handling Mechanism.	L3
<b>Module III:</b> Memory Management Strategies – Contiguous and Non-Contiguous allocation , Virtual memory Management –Demand Paging, Page Placement and Replacement Policies	<b>CO4-</b> Analyze memory management schemes and Demand Paging, Page Placement and Replacement Policies.	L3
<b>Module IV:</b> File System –Basic concepts , File System design and Implementation ,Case Study: Linux File Systems , Mass Storage Structure – Disk Scheduling , Disk Management –I/O Systems, System Protection and Security.	<b>CO5-</b> Understand I/O management and File systems and basics of Linux system and Mobile OS like iOS and Android.	L4
<b>Module V:</b> Distributed Systems –Distributed operating systems , Distributed file systems, Distributed Synchronization	<b>CO6-</b> Analyze the concept of Distributed operating systems, Distributed file systems and synchronization mechanism in distributed operating system.	L3



# SYNERGY INSTITUTE OF TECHNOLOGY, BHUBANESWAR

Approved by AICTE, New Delhi, Affiliated to BPUT, Odisha

## Department of Computer Science and Engineering

**Subject: Artificial Intelligence & Machine Learning**

**Year/Semester: 3<sup>rd</sup> / 5<sup>th</sup>**

**Subject Code: C304**

Module	CO Statement After completion of the course the student will be able to	BTL
<b>Module 1:</b> INTRODUCTION –The Foundations of Artificial Intelligence; - INTELLIGENT AGENTS – Agents and Environments, Good Behaviour: The Concept of Rationality, the Nature of Environments, the Structure of Agents,	<b>CO1-</b> Explain fundamental concepts and challenges in AI to acquaint students with the meaning, purpose, scope, applications, and effects of AI	<b>L2</b>
<b>Module 1:</b> SOLVING PROBLEMS BY SEARCH – Problem-Solving Agents, Formulating problems, Searching for Solutions, Uninformed Search Strategies, Breadth-first search, Depth-first search, Searching with Partial Information, Informed (Heuristic) Search Strategies, Greedy best-first search, A* Search, CSP, Means-End-Analysis.	<b>CO2-</b> Apply search methods that agents can employ for problem solving.	<b>L3</b>
<b>Module 2 :</b> ADVERSARIAL SEARCH – Games, The Mini-Max algorithm, optimal decisions in multiplayer games, Alpha-Beta Pruning, Evaluation functions, Cutting off search, ,		
<b>Module 2:</b> Knowledge-Based agents, Propositional Logic, Reasoning Patterns in Propositional Logic, Resolution, Forward and Backward chaining - FIRST ORDER LOGIC – Syntax and Semantics of First-Order Logic, Using First-Order Logic , Knowledge Engineering in First-Order Logic - INFERENCE IN FIRST ORDER LOGIC – Propositional vs. First-Order Inference, Unification and Lifting, Resolution	<b>CO3-</b> Interpret the knowledge using logic concepts.	<b>L5</b>
<b>Module 3:</b> UNCERTAINTY – Acting under Uncertainty, Basic Probability Notation, The Axioms of Probability, Inference Using Full Joint Distributions, Independence, Bayes' Rule and its Use, PROBABILISTIC REASONING – Representing Knowledge in an Uncertain Domain, The Semantics of Bayesian Networks, Efficient Representation of Conditional Distribution, Exact Inference in Bayesian Networks, Approximate Inference in Bayesian Networks	<b>CO4-</b> Create representations of the domain of interest and reason with these representations and infer solutions using probabilistic methods	<b>L3</b>



# SYNERGY INSTITUTE OF TECHNOLOGY, BHUBANESWAR

Approved by AICTE, New Delhi, Affiliated to BPUT, Odisha

## Department of Computer Science and Engineering

SYNERGY.BHUBANESWAR

<b>Module 4:</b> LEARNING METHODS – Statistical Learning, Learning with Complete Data, Learning with Hidden Variables, Rote Learning, Learning by Taking Advice, Learning in Problem-solving, learning from Examples: Induction, Explanation-based Learning, Discovery, Analogy, Formal Learning Theory, Neural Net Learning and Genetic Learning.	<b>CO5-</b> Understand how machine learning algorithms works for real life problems	<b>L3</b>
<b>Module 4:</b> Expert Systems: Representing and Using Domain Knowledge, Expert System Shells, Explanation, Knowledge Acquisition.	<b>CO6-</b> Comprehend AI & Expert System to analyze and map real world activities to digital world	<b>L4</b>



# SYNERGY INSTITUTE OF TECHNOLOGY, BHUBANESWAR

Approved by AICTE, New Delhi, Affiliated to BPUT, Odisha

## Department of Computer Science and Engineering

**Subject: Computer Graphics**

**Year/Semester: 3<sup>rd</sup> / 5<sup>th</sup>**

**Subject Code: C305**

Module	CO Statement After completion of the course the student will be able to	BTL
<b>Module I:</b> Basic of Computer Graphics: Applications of computer graphics, Display device	<b>CO1-</b> Understand the Application of Computer Graphics and to know the mechanism of its display devices.	L2
<b>Module II:</b> Graphics Primitives: Points, lines, circles and ellipses as primitives, scan conversion algorithms for primitives, Fill area primitives including scan-line polygon filling, inside-outside test, boundary and flood-fill, character generation, line attributes, area-fill attributes, character attributers.	<b>CO2-</b> Conceptualize various primitives algorithms and fill area primitives algorithm.	L3
<b>Module III:</b> 2D transformation and viewing: Transformations, matrix representation, viewing pipeline and coordinates system, window-to-viewport transformation, clipping including point clipping, line clipping, polygon clipping	<b>CO3-</b> Analyze various 2D transformations, viewing and clipping techniques.	L4
<b>Module IV:</b> 3D concepts and object representation: 3D transformation and viewing: 3D scaling, rotation and translation, composite transformation.	<b>CO4-</b> Analyze the representation of different types of 3D transformation and viewing concepts	L4
<b>Module V:</b> Advance topics: visible surface detection concepts, back-face detection, depth buffer method, illumination, light sources, illumination methods, Color models: properties of light, XYZ, RGB, YIQ and CMY color models	<b>CO5</b> –Analyze the techniques of common visible surface detection methods.	L5
	<b>CO6-</b> Understand the concept regarding Illumination models and polygon surface rendering method	L4



# SYNERGY INSTITUTE OF TECHNOLOGY, BHUBANESWAR

Approved by AICTE, New Delhi, Affiliated to BPUT, Odisha

## Department of Computer Science and Engineering

**Subject: Universal Human Values**

**Year/Semester: 3<sup>rd</sup> / 5<sup>th</sup>**

**Subject Code: C306**

<b>Module</b>	<b>CO Statement After completion of the course the student will be able to</b>	<b>BTL</b>
<b>MODULE I: FOUNDATIONS OF VALUE EDUCATION</b> -Right understanding, Relationship and Physical Facility (Holistic development and the Role of Education), Understanding Value Education	<b>CO1:</b> Develop a holistic perspective based on self-exploration through value education.	<b>L6</b>
Self-exploration as the Process for Value Education, Continuous Happiness and Prosperity, The Basic Human Aspirations, Happiness and Prosperity- Current Scenario, Method to Fulfill the Basic Human Aspirations.	<b>CO2:</b> Develop an understanding of the Self, the basic human aspirations and methods of fulfilling them.	<b>L6</b>
<b>MODULE II: HARMONY IN THE HUMAN LIFE, RELATIONSHIPS AND SOCIETY.</b> - Understanding Human being as the Co-existence of the Self and the Body, Distinguishing between the needs of the Self and the Body, Achieving Harmony:	<b>CO3:</b> Develop an understanding or gain clarity of the harmony in the human being as a co-existence of the Self and the Body.	<b>L2</b>
-Integrating Self and the Body, Harmony in the Family and Society, 'Trust' and 'Respect'-- as Foundational Values in Relationship, Other Feelings, Justice in Human to Human Relationship, Understanding Harmony in the Society and Universal Human Order.	<b>CO4:</b> Develop an understanding of the relationship with Family, society and nature/existence. Expanding the feeling of harmony from within to the same with the rest of the world.	<b>L6</b>
<b>MODULE III: HARMONY IN THE NATURE /EXISTENCE AND PROFESSIONAL ETHICS.</b>  -Understanding Harmony in the Nature, Interconnectedness, self-regulation and Mutual Fulfillment among the Four Orders of Nature, Realizing Existence as Co-existence at All Levels, The Holistic Perception of Harmony in Existence, Natural Acceptance of Human Values, Humanistic Education, Humanistic Constitution and Universal Human Order.	<b>CO5:</b> Develop an understanding of the relationship of synthesis and mutual fulfillment with nature and the world order. An awareness of the eco-system, one's place in and responsibility towards the same.	<b>L2</b>



# SYNERGY INSTITUTE OF TECHNOLOGY, BHUBANESWAR

Approved by AICTE, New Delhi, Affiliated to BPUT, Odisha

## Department of Computer Science and Engineering

SYNERGY.BHUBANESWAR

-Competence in Professional Ethics- Ethical Decision Making and Transition towards Value-based Life and Profession.	<b>CO6:</b> Adapt to the present-day work scenario with knowledge of professional ethics, decision making, progressive and sustainable living.	<b>L6</b>
---------------------------------------------------------------------------------------------------------------------	------------------------------------------------------------------------------------------------------------------------------------------------	-----------



# SYNERGY INSTITUTE OF TECHNOLOGY, BHUBANESWAR

Approved by AICTE, New Delhi, Affiliated to BPUT, Odisha

## Department of Computer Science and Engineering

**Subject: Formal Language and Automata Theory Lab**

**Year/Semester: 3<sup>rd</sup> / 5<sup>th</sup>**

**Subject Code: C307**

Module	CO Statement After completion of the course the student will be able to	BTL
Implement DFA for Regular language.	<b>CO1-</b> Gain a deep understanding of automata theory, formal language processing, and the foundations of computational theory, which are crucial for designing efficient algorithms and recognizing patterns in strings.	L3
Apply conversion techniques DFA and NFA	<b>CO2-</b> Gain insights into the equivalence of different computational models and enhance their skills in automata theory, formal language design, and the simplification of state machines.	L3
Able to do DFA minimization.	<b>CO3-</b> Able to optimize finite automata, improving their understanding of computational efficiency, formal language theory, and algorithmic design in the context of automata and language processing.	L4
Able to use PDA for context free language	<b>CO4-</b> Gain proficiency in parsing and recognizing syntactic structures, deepening their understanding of formal grammars, computational complexity.	L3
Able to design and develop CYK parsing algorithm for context free grammar able to design and develop Turing machine for some recursive language.	<b>CO5-</b> Gain advanced skills in computational theory and algorithmic design, enabling them to solve complex problems in language parsing, formal language theory, and theoretical computer science.	L3
	<b>CO6-</b> Gain a profound understanding of computability theory, algorithmic complexity, and the theoretical limits of computation, equipping them with advanced skills in problem-solving and theoretical computer science.	L4



# SYNERGY INSTITUTE OF TECHNOLOGY, BHUBANESWAR

Approved by AICTE, New Delhi, Affiliated to BPUT, Odisha

## Department of Computer Science and Engineering

**Subject: Database Management System Lab**

**Year/Semester: 3<sup>rd</sup> / 5<sup>th</sup>**

**Subject Code: C308**

Experiment Name	CO Statement After completion of the course the student will be able to	BTL
1. Use of SQL syntax: insertion, deletion, join, updation using SQL.	CO1- Use of SQL Syntax.	L4
2. Programs on join statements and SQL queries including where clause.	CO2- Able to use join statements in programs	L4
3. Programs on database triggers. 4. Programs on packages.	CO3- To be familiar and create program using database triggers and packages	L6
5. Programs on data recovery using check point technique	CO4- Able to use data recovery using check point technique	L4
6. Concurrency control problem using lock operations.	CO5- Able to design and develop Concurrency control problem using lock operation	L6
7. Programs on ODBC using either VB or VC++. 8. Programs on JDBC.	CO6- Able to design and develop ODBC, JDBC And embedded SQL using C/C++	L6



# SYNERGY INSTITUTE OF TECHNOLOGY, BHUBANESWAR

Approved by AICTE, New Delhi, Affiliated to BPUT, Odisha

## Department of Computer Science and Engineering

**Subject: Operating System Lab**

**Year/Semester: 3<sup>rd</sup> / 5<sup>th</sup>**

**Subject Code: C309**

<b>Experiment Name</b>	<b>CO Statement After completion of the course the student will be able to</b>	<b>BTL</b>
1.Basic UNIX Commands. Linux Administrative commands.	<b>CO 1-</b> Learn basic Unix commands and Linux Administrative commands.	L3
2.UNIX Shell Programming and Programs on UNIX System calls.	<b>CO 2 -</b> Write shell program for process and file system management with system calls.	L3
3.Programs on process creation and synchronization, inter process communication including shared memory, pipes and messages. (Dinning Philosopher problem / Cigarette Smoker problem / Sleeping barber problem)	<b>CO 3 -</b> Write Programs on process creation and synchronization, inter process communication including shared memory, pipes and messages .	L3
4.Simulation of CPU Scheduling Algorithms. (FCFS, RR, SJF, Priority, Multilevel Queuing)	<b>CO 4-</b> Perform lab on CPU Scheduling Algorithms like FCFS, RR, SJF.	L4
5.Simulation of Banker's Algorithm for Deadlock Avoidance, Prevention.	<b>CO 5 -</b> Perform lab on Banker's Algorithm for Deadlock Avoidance, Prevention.	L3
6.Program for FIFO, LRU, and OPTIMAL page replacement algorithm.	<b>CO 6 -</b> Write Programs for FIFO, LRU, and OPTIMAL page replacement algorithm.	L4



# SYNERGY INSTITUTE OF TECHNOLOGY, BHUBANESWAR

Approved by AICTE, New Delhi, Affiliated to BPUT, Odisha

## Department of Computer Science and Engineering

**Subject: Evaluation Of Summer Internship**

**Year/Semester: 3<sup>rd</sup> / 5<sup>th</sup>**

**Subject Code: C310**

	<b>CO Statement</b> <b>After completion of the course the student will be able to</b>	<b>BTL</b>
<b>CO 1</b>	It focus on what students intend to accomplish for the internship organization during their work term.	L3
<b>CO 2</b>	It describes what students will add to their knowledge, skills and development in that area.	L3
<b>CO 3</b>	It describes the students punctuality at the place of internship how they will perform their duties.	L4
<b>CO 4</b>	The activity is the expected achievement, within the time frame expected completion with date and the evaluation is the stated method of measurement.	L4
<b>CO 5</b>	It gives a chance to gain real-world experience beyond classrooms.	L4



# SYNERGY INSTITUTE OF TECHNOLOGY, BHUBANESWAR

Approved by AICTE, New Delhi, Affiliated to BPUT, Odisha

## Department of Computer Science and Engineering

**Subject: Software Engineering**

**Year/Semester: 3<sup>rd</sup> / 6<sup>th</sup>**

**Subject Code: C311**

Module	CO Statement After completion of the course the student will be able to	BTL
<b>Module I:</b> Software Process Models: Software Product, Software crisis, Handling complexity through Abstraction and Decomposition, Overview of software development activities, Process Models, Classical waterfall model, iterative waterfall model, prototyping mode, evolutionary model, spiral model, RAD model, Agile models: Extreme Programming, and Scrum.	<b>CO1-</b> Know various software characteristics and analyze different software Development Models.	L2
<b>Module II:</b> Software Requirements Engineering: Requirement Gathering and Analysis, Functional and Non-functional requirements, Software Requirement Specification (SRS), IEEE 830 guidelines, Decision tables and trees. Structured Analysis & Design: Overview of design process, High-level and detailed design, Cohesion and coupling, Modularity and layering, Function-Oriented software design: Structured Analysis using DFD Structured Design using Structure Chart, Basic concepts of Object Oriented Analysis & Design. User interface design, Command language, menu and iconic interfaces.	<b>CO2-</b> Demonstrate the contents of a SRS and apply basic software quality assurance practices to ensure that design, development meet or exceed applicable standards.  <b>CO3-</b> Develop, maintain and evaluate large-scale software systems	L3  L5
<b>Module III:</b> Coding and Software Testing Techniques: Coding, Code Review, documentation. Testing: - Unit testing, Black-box Testing, White-box testing, Cyclomatic complexity measure, coverage analysis, mutation testing, Debugging techniques, Integration testing, System testing, Regression testing. Software Reliability and Software	<b>CO4-</b> Formulate testing strategy for software systems using methods like functional testing, test driven development, and unit testing.	L5
<b>Module IV:</b> Maintenance: Basic concepts in software reliability, reliability measures, reliability growth modelling, Quality SEI CMM, Characteristics of software maintenance, software reverse engineering, software reengineering, software reuse. Emerging Topics: Client-Server Software Engineering, Service-oriented Architecture (SOA), and Software as a Service (SaaS)	<b>CO5-</b> Produce efficient, reliable, robust and cost-effective software solutions.  <b>CO6-</b> Utilize and manage various software management tools for development, maintenance, and analysis while managing the software development process both individually and in teams.	L3  L4



# SYNERGY INSTITUTE OF TECHNOLOGY, BHUBANESWAR

Approved by AICTE, New Delhi, Affiliated to BPUT, Odisha

## Department of Computer Science and Engineering

**Subject: Compiler Design**

**Year/Semester: 3<sup>rd</sup> / 6<sup>th</sup>**

**Subject Code: C312**

Module	CO Statement After the successful completion of the course, students will be able to:	BTL
<b>Module 1:</b> Introduction: Overview and Phases of compilation. Lexical Analysis: Non-Deterministic and Deterministic Finite Automata (NFA & DFA), Regular grammar, Regular expressions and Regular languages,	<b>CO1</b> - Visualize the different phases of compilation by applying knowledge of system programming and mathematics to solve problems related to language translation.	<b>L2</b>
<b>Module 1:</b> Design of a Lexical Analyzer as a DFA, Lexical Analyzer generator. Syntax Analysis: Role of a Parser, Context free grammars and Context free languages,	<b>CO2-</b> Develop and understanding of their use for engineering practice for design of Lexical analyzers by using tools like LEX.	<b>L3</b>
<b>Module 1 :</b> Parse trees and derivations, Ambiguous grammar. Top Down Parsing: Recursive descent parsing, LL (1) grammars, Non-recursive Predictive Parsing, Error reporting and Recovery. Bottom Up Parsing: Handle pruning and shift reduces Parsing, SLR parsers and construction or SLR parsing tables, LR(1) parsers and construction of LR(1) parsing tables, LALR parsers and construction of efficient LALR parsing tables, Parsing using Ambiguous grammars, Error reporting and Recovery, Parser generator	<b>CO3-</b> Formulate machine code by considering the system design components and functionalities involved in compilation by designing Parsers(LL, LR, CLR & LALR) using YACC tools as parser generator	<b>L5</b>
<b>Module 2:</b> Intermediate Code Generation: DAG for expressions, Three address codes - Quadruples and Triples, Types and declarations, Translation of Expressions, Array references, Type checking and Conversions, Translation of Boolean expressions and control flow statements, Back Patching, Intermediate Code Generation for Procedures.	<b>CO4-</b> Develop skills in generating syntax directed translation and different methods of intermediate code representation.	<b>L4</b>
<b>Module 3:</b> Code Generation: Factors involved, Registers allocation, Simple code generation using STACK Allocation, Basic blocks and flow graphs, Simple code generation using flow graphs. Code Optimization: Objective, Peephole Optimization, and Concepts of Elimination of local common sub-expressions, Redundant and un-reachable codes, Basics of flow of control optimization.	<b>CO5-</b> Build an environment for compilation and code generation.	<b>L2</b>
<b>Module 4:</b> Run Time Environment: Storage Organizations, Static and Dynamic Storage	<b>CO6-</b> Inspect runtime structure and analyze how to develop code & design a	<b>L4</b>



# SYNERGY INSTITUTE OF TECHNOLOGY, BHUBANESWAR

Approved by AICTE, New Delhi, Affiliated to BPUT, Odisha

## Department of Computer Science and Engineering

SYNERGY.BHUANESWAR

Allocations, STACK Allocation, Handlings of activation records for calling sequences. Syntax Directed Translation: Syntax Directed Definitions (SDD), Inherited and Synthesized Attributes, Dependency graphs, Evaluation orders for SDD, Semantic rules, Application of Syntax Directed Translation. Symbol Table: Structure and features of symbol tables, symbol attributes and scopes.	compiler for concise programming language.	
--------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------	--------------------------------------------	--



# SYNERGY INSTITUTE OF TECHNOLOGY, BHUBANESWAR

Approved by AICTE, New Delhi, Affiliated to BPUT, Odisha

## Department of Computer Science and Engineering

**Subject: Optimization in Engineering**

**Year/Semester: 3<sup>rd</sup> / 6<sup>th</sup>**

**Subject Code: C313**

Module	CO Statement After the successful completion of the course, students will be able to:	BTL
<b>MODULE 1:</b> Idea of Engineering optimization Problems Classification of optimization algorithms modeling of problems and principle of modeling .Linear Programming: Formulation of LPP, Graphical solution, Simplex method, Big-M method, Revised simplex method, Duality theory and its application, Dual simplex method, Sensitivity analysis in linear Programming.	<b>CO1-</b> Understand various methods for salutation of LPP  <b>CO2-</b> Study and analyze different cases of LPP	<b>L2</b>  <b>L4</b>
<b>MODULE 2:</b> Transportation problems Finding an initial basic feasible solution by Northwest Corner rule, Least Cost rule, Vogel's approximation method, Degeneracy, Optimality test, MODI method Stepping stone method. Assignment problems: Hungarian method for solution of Assignment problems Integer Programming: Branch and Bound algorithm for solution of integer programming problems.	<b>CO3-</b> Enrich fundamental concept to solve assignment problem and T.P. problem	<b>L3</b>
<b>MODULE 3:</b> Introduction to non-linear programming. Unconstraint optimization: Fibonacci and Golden Section Search method. Constrained optimization with equality constraint: Lagrange multiplier, Projected gradient method. Constrained optimization with inequality constraint: Kuhn-Tucker condition, Quadratic programming. Unconstraint optimization: Fibonacci and Golden Section Search method. Constrained optimization with equality constraint Lagrange multiplier, Projected gradient method. Constrained optimization with inequality constraint: Kuhn-Tucker condition Quadratic programming .	<b>CO4-</b> Analyze N.L.P.P. and salutation and Details.	<b>L4</b>
<b>MODULE 4:</b> Queuing models: General characteristics, Markovian queuing model, M/M/1model, Limited queue capacity, multiple server, Finite sources, Queue discipline. General characteristics, Markovian queuing model M/M/1model, Limited queue capacity, multiple server Finite sources Queue discipline.	<b>CO5-</b> Understand the fundamental concept of queue problem LPP and salutation.  <b>CO6-</b> Assessment different NLPP linked with different method of salutation.	<b>L2</b>  <b>L5</b>



# SYNERGY INSTITUTE OF TECHNOLOGY, BHUBANESWAR

Approved by AICTE, New Delhi, Affiliated to BPUT, Odisha

## Department of Computer Science and Engineering

**Subject: Compiler Design**

**Year/Semester: 3<sup>rd</sup> / 6<sup>th</sup>**

**Subject Code: C314**

Sl. No.	Module	After the successful completion of the course, students will be able to:	BTL
1	<b>Module 1:</b> Introduction: Overview and Phases of compilation. Lexical Analysis: Non-Deterministic and Deterministic Finite Automata (NFA & DFA), Regular grammar, Regular expressions and Regular languages,	<b>CO1</b> - Visualize the different phases of compilation by applying knowledge of system programming and mathematics to solve problems related to language translation.	2
2	<b>Module 1:</b> Design of a Lexical Analyzer as a DFA, Lexical Analyzer generator. Syntax Analysis: Role of a Parser, Context free grammars and Context free languages,	<b>CO2-</b> Use of compiler phases to develop an understanding of their use for engineering practice for design of Lexical analyzers by using tools like LEX.	3
3	<b>Module 1 :</b> Parse trees and derivations, Ambiguous grammar. Top Down Parsing: Recursive descent parsing, LL (1) grammars, Non-recursive Predictive Parsing, Error reporting and Recovery. Bottom Up Parsing: Handle pruning and shift reduces Parsing, SLR parsers and construction or SLR parsing tables, LR(1) parsers and construction of LR(1) parsing tables, LALR parsers and construction of efficient LALR parsing tables, Parsing using Ambiguous grammars, Error reporting and Recovery, Parser generator	<b>CO3-</b> Formulate machine code by considering the system design components and functionalities involved in compilation by designing Parsers(LL, LR, CLR & LALR) using YACC tools as parser generator	5
4	<b>Module 2:</b> Intermediate Code Generation: DAG for expressions, Three address codes - Quadruples and Triples, Types and declarations, Translation of Expressions, Array references, Type checking and Conversions, Translation of Boolean expressions and control flow statements, Back Patching, Intermediate Code Generation for Procedures.	<b>CO4-</b> Develop skills in generating syntax directed translation and different methods of intermediate code representation.	4
5	<b>Module 3:</b> Code Generation: Factors involved, Registers allocation, Simple code generation using STACK Allocation, Basic blocks and flow graphs, Simple code generation using flow graphs. Code Optimization: Objective, Peephole Optimization, and Concepts of Elimination of local common sub-expressions, Redundant and un-reachable codes, Basics of flow of control optimization.	<b>CO5-</b> Building an environment for compilation and code generation.	2
6	<b>Module 4:</b> Run Time Environment: Storage Organizations, Static and Dynamic Storage Allocations, STACK Allocation, Handlings of activation records for calling sequences. Syntax Directed Translation: Syntax Directed Definitions (SDD), Inherited and Synthesized Attributes, Dependency graphs, Evaluation orders for SDD, Semantic rules, Application of Syntax Directed Translation. Symbol Table: Structure and features of symbol tables, symbol attributes and scopes.	<b>CO6-</b> Inspect runtime structure and analyze how to develop code & design a compiler for concise programming language.	4



# SYNERGY INSTITUTE OF TECHNOLOGY, BHUBANESWAR

Approved by AICTE, New Delhi, Affiliated to BPUT, Odisha

## Department of Computer Science and Engineering

**Subject: Real-Time System**

**Year/Semester: 3<sup>rd</sup> / 6<sup>th</sup>**

**Subject Code: C314**

Module	CO Statement After the successful completion of the course, students will be able to:	BTL
<b>Module-I:</b> Introduction: What is real-time, Applications of Real-Time systems, A basic model of Real-time system, Characteristics of Real-time system, Safety and Reliability, Types of Real-time tasks, timing constraints, Modelling timing constraints Real-Time Task Scheduling: Some important concepts, Types of Real-time tasks and their characteristics, Task scheduling, Clock-Driven scheduling, Hybrid schedulers, Event Driven scheduling, Earliest Deadline First (EDF) scheduling, Rate monotonic algorithm (RMA). Some issues Associated with RMA. Issues in using RMA practical situations	<b>CO1-</b> Understand concepts of real-time system	L2
<b>Module-II:</b> Handling Resource Sharing and dependencies among Real-time Tasks: Resource sharing among real time tasks. Priority inversion. Priority Inheritance Protocol (PIP), Highest Locker Protocol (HLP). Priority Ceiling Protocol (PCP). Different types of priority inversions under PCP. Important features of PCP. Some issues in using a resource sharing protocol. Handling task dependencies. Scheduling Real-time tasks in multiprocessor and distributed systems: Multiprocessor task allocation, Dynamic allocation of tasks. Fault tolerant scheduling of tasks. Clock in distributed Real-time systems, Centralized clock synchronization	<b>CO2-</b> Understand the different protocol in Real Time OS	L2
<b>Module-III:</b> Commercial Real-time operating systems: Time services, Features of a Real-time operating system, Unix as a Real-time operating system, Unix-based Real-time operating systems, Windows as a Real time operating system, POSIX-RT, A survey of contemporary Real-time operating systems. Benchmarking real-time systems.	<b>CO3-</b> Analyze the different real-time OS task	L4
	<b>CO4-</b> Understand the concepts of different types of commercial OS	L2
<b>Module IV :</b> Real-time Databases: Example applications of Real-time databases. Review of basic database concepts, Real-time databases, Characteristics of temporal data. Concurrency control in real-time databases. Commercial real-time databases. Real-time Communication: Basic concepts, Examples of applications, Real-time communication in a LAN and Real-time communication over packet switched networks.	<b>CO5-</b> Work out and apply real-time database	L3
	<b>CO6-</b> Understand and Analyze the concepts of Basic Real Time Communication Architecture	L3



# SYNERGY INSTITUTE OF TECHNOLOGY, BHUBANESWAR

Approved by AICTE, New Delhi, Affiliated to BPUT, Odisha

## Department of Computer Science and Engineering

**Subject: Software Engineering Lab**

**Year/Semester: 3<sup>rd</sup> / 6<sup>th</sup>**

**Subject Code: C317**

Experiment	CO Statement After the successful completion of the course, students will be able to:	BTL
1. Develop requirements specification for a given problem. (The requirements specification should include both functional and non-functional requirements. For a set of about 20 sample problems,)	<b>CO1:</b> Prepare SRS document, design document, test cases and software configuration management and risk management related document	<b>L6</b>
2. Develop DFD Model (Level 0, Level 1 DFD and data dictionary) of the sample problem 3. Develop structured design for the DFD model developed.	<b>CO2:</b> Create a DFD (Data Flow Diagram) model for a sample problem involves breaking down the system into its various processes and data flows	<b>L6</b>
4. Develop UML use case model for a problem (use of case tool like rational rose)	<b>CO3:</b> Develop function oriented and object oriented software design using tools like rational rose	<b>L6</b>
5. Develop sequence diagram. 6. Develop class diagram. 7. Develop code for the developed class model using Java.	<b>CO4:</b> Develop the Class Diagram, Sequence Diagrams & Java code based on the class model.	<b>L6</b>
8. Use testing tool such as Junit.	<b>CO5:</b> Perform unit testing and integration testing	<b>L3</b>
9. Using configuration management tools. 10. Use any one project management tool such as Microsoft project, Gantt project or project Libre.	<b>CO6:</b> Apply various white box and black box testing techniques	<b>L3</b>



# SYNERGY INSTITUTE OF TECHNOLOGY, BHUBANESWAR

Approved by AICTE, New Delhi, Affiliated to BPUT, Odisha

## Department of Computer Science and Engineering

**Subject: Compiler Design Lab**

**Year/Semester: 3<sup>rd</sup> / 6<sup>th</sup>**

**Subject Code: C318**

Course Outcome	Course Outcome Statements	BTL
CO318.1	How to create a DFA from a given regular expression & Read a regular expression in standard form and check the error.	L2
CO318.2	How to create a NFA from a given regular expression & Read a regular expression in standard form and check the error.	L2
CO318.3	To Use Lex and yacc to extract tokens from a given source code	L3
CO318.4	To know how to Write a suitable data structure to store a Context Free Grammar.	L2
CO318.5	To create LL(1) parse table & create SLR(1) parse table for a given grammar by Simulator.	L4



# SYNERGY INSTITUTE OF TECHNOLOGY, BHUBANESWAR

Approved by AICTE, New Delhi, Affiliated to BPUT, Odisha

## Department of Computer Science and Engineering

Subject: Future Ready Contributor Develop Model Lab

Year/Semester: 3<sup>rd</sup> / 6<sup>th</sup>

Subject Code: C319

Modules	CO Statement After the successful completion of the course, students will be able to:	BTL
<b>MODULE I: DEVELOPING SELF-EFFICACY &amp; BASIC INNER STRENGTH</b> Who is Future-Ready contributor?, Self-esteem & Growth Identity, Become a creator of one's destiny	<b>CO1:</b> Understand & Apply the concepts related to self-efficacy, growth identity, and the shift from a victim mindset to a creator mindset.	<b>L2, L3</b>
<b>MODULE II: BUILDING ABILITY TO MAKE MORE EFFECTIVE CAREER CHOICES</b> -Achieving Sustainable Success -Career Development Pathways for a changing world, Make and impact in every part of one's life.	<b>CO2:</b> Innovate and create their own paths to long-term success by developing their own frame works for long-term career fulfillment and personal growth. <b>CO3:</b> Create their "engine of success" and explore diverse career development pathways & to generate unique solutions tailored to their individual aspirations and circumstances. To achieve creativity and originality in their approach to achieving sustainable success.	<b>L4</b>  <b>L5</b>
<b>MODULE III: BUILDING ABILITY TO BECOME SOLUTION &amp; VALUE CREATING INDIVIDUALS IN WORLDS</b> -Think Solutions -Value Thinking, Engaging Deeply	<b>CO4:</b> Create and develop a mindset focused on finding solutions rather than dwelling on problems, thereby being equipped to thrive in today's rapidly changing market landscape. <b>CO5:</b> Analyze and create valuable contributions by identifying what matters to organizations and customers, as well as foster deep engagement with complex environments.	<b>L5</b>
<b>MODULE IV: BUILDING ABILITY TO WORK COLLABORATIVELY &amp; AS GOOD CITIZENS OF ORGANIZATIONS &amp; THE COUNTRY</b> Enlightened self-interest & collaboration at work, Human-centered thinking & Empathy, Trust Conduct.	<b>CO6:</b> Apply practical skills and strategies for effectively collaborating in teams and contributing positively to organizations and society.	<b>L3</b>



# SYNERGY INSTITUTE OF TECHNOLOGY, BHUBANESWAR

Approved by AICTE, New Delhi, Affiliated to BPUT, Odisha

## Department of Computer Science and Engineering

**Subject: Seminar-I**

**Year/Semester: 3<sup>rd</sup> / 6<sup>th</sup>**

**Subject Code: C320**

<b>CO Statement</b> <b>After the successful completion of the course, students will be able to:</b>	<b>BTL</b>
CO1--Outline the topics on modern technology, Prepare implementation of the same as the presentation.	L2
CO2-Understanding the technologies used by extracting the new things to be implemented by reviewing the journals/research papers.	L3
CO3-Sketch the application of the technology for the use of the mankind.	L4
CO4- Analyze and correlate the new technology with the subject of interest for further study.	L4
CO5- Evaluate, plan and reframe the technology with the communication skills for a better explanation and presentation.	L3
CO6-Modify and design the concept into the realistic world.	L3



# SYNERGY INSTITUTE OF TECHNOLOGY, BHUBANESWAR

Approved by AICTE, New Delhi, Affiliated to BPUT, Odisha

## Department of Computer Science and Engineering

**Subject: Entrepreneurship Development**

**Year/Semester: 4<sup>th</sup> / 7<sup>th</sup>**

**Subject Code: C401**

Modules	CO Statement After the successful completion of the course, students will be able to:	BTL
<b>Module 1:</b> Concept of Entrepreneurship & Intrapreneurship, Nature, Process and importance of Entrepreneurship Types of Entrepreneurs, Entrepreneurial Traits and Skills, Entrepreneurial Personality Entrepreneurial Motivation and Achievement	<b>CO1-</b> Understand the Concept of Entrepreneurship, Startups & Incubation, Types of Entrepreneurs. Develop the entrepreneurial Skills and personality to be successful Entrepreneur.	L1,L2
<b>Module – 2:</b> Entrepreneurial Environment, Identification of Opportunities, Converting business opportunity into reality Setting up of a Small Enterprise, Issues relating to location Startups and business incubation	<b>CO2-</b> Analyze business environment to identify business opportunities, know the methods of conducting market survey and preparation of Preliminary & Detailed Project Report.	L5,L6
<b>Module-3:</b> Environmental problems and Environmental Pollution Act, Industrial Policies and Regulations Industrial Policy Resolution 1956.	<b>CO3-</b> Learn about the Environment Protection Acts, rules and regulations to start a new venture.	L3
<b>Module-4:</b> Need to know about accounting and Working Capital Management Marketing Management Human Resource Management and Labour Laws Organizational Support Services-Central & State Government Incentives and Subsidies	<b>CO4-</b> Develop Entrepreneurial Strategies(Marketing, Finance, Human Resource Management Accounting ) to manage an enterprise and also know the various Government Schemes which support Entrepreneurs	L4,L3
<b>Module-5:</b> Sickness of Small Scale Industries, Causes and Symptoms of sickness	<b>CO5-</b> Identify the causes of industrial sickness and areas to implement remedial measures to revitalize and revive sick industries for sustainable growth of Indian Economy	L5,L3
<b>Module-6:</b> Cure of Sickness and Role of Banks and Government in reviving Industries	<b>CO6-</b> Learn to form associations and negotiate with Government and Banks to help these sick industries get adequate funds to revive their sick units.	L6



# SYNERGY INSTITUTE OF TECHNOLOGY, BHUBANESWAR

Approved by AICTE, New Delhi, Affiliated to BPUT, Odisha

## Department of Computer Science and Engineering

**Subject: Embedded System**

**Year/Semester: 4<sup>th</sup> / 7<sup>th</sup>**

**Subject Code: C402**

Modules	CO Statement After the successful completion of the course, students will be able to:	BTL
<b>Module-1:</b> Hardware Concepts Embedded System: Application and characteristics of embedded systems, Overview of Processors and hardware units in embedded system, embedded software in a system, Examples of Embedded system.	<b>CO1:</b> Study different embedded processors and system.	L2
<b>Module -2:</b> ARM:ARM pipeline, Instruction Set Architecture ISA: Registers, Data Processing Instructions, Data Transfer Instructions, Multiplication's instructions, Software interrupt, Conditional execution, branch instruction, Swap instruction, THUMB instructions.	<b>CO2:</b> Emphasize knowledge about ARM pipeline and ISA architecture.	L3
<b>Module-3:</b> Devices and device drivers: I/O devices, Serial peripheral interfaces, IIC, RS232C, RS422, RS485, Universal serial bus, USB Interface, USB Connector IrDA, CAN, Bluetooth, ISA, PCI, PCI -X and advance busses, Device drivers.	<b>CO3:</b> Study different interfacing devices and drivers	L2
<b>Module-4:</b> Real Time Operating System (RTOS): Real-Time Task Scheduling: Some important concepts, Types of real-time tasks and their characteristics, Task scheduling, Clock-Driven scheduling, Hybrid schedulers, Event-Driven scheduling, Earliest Deadline First (EDF) scheduling, Rate monotonic algorithm (RMA)	<b>CO4:</b> Acquire in-depth knowledge about real time operating system.	L3
<b>Module-5:</b> Modelling Techniques: Software and programming concept: Processor selection for an embedded system, State chart, SDL, Petri-Nets, Unified Modeling Language (UML). Hardware software design. Hardware and software partitioning: K-L partitioning, Partitioning using genetic algorithm,	<b>CO5 :</b> Focus on model designing and programming of embedded system	L3
<b>Module-6:</b> Low power embedded system design: Dynamic power dissipation, Static power dissipation, Power reduction techniques, system level power management. Software design for low power devices.	<b>CO6:</b> study of low power embedded system.	L2



# SYNERGY INSTITUTE OF TECHNOLOGY, BHUBANESWAR

Approved by AICTE, New Delhi, Affiliated to BPUT, Odisha

## Department of Computer Science and Engineering

**Subject: Software Project Management**

**Year/Semester: 4<sup>th</sup> / 7<sup>th</sup>**

**Subject Code: C403**

Modules	CO Statement After the successful completion of the course, students will be able to:	BTL
<b>Module I:</b> Introduction to Software Project Management - Software Projects - ways of categorizing software projects – problems with software projects - Project Life Cycle – Management - Setting objectives – Stakeholders - Project Team – Step Wise: An overview of project planning - Project evaluation - Selection of appropriate project approach. S/W size estimation, estimation of effort & duration. COCOMO models, Putnam's work, Jensen's model, Halstead's software Science.	<b>CO1-</b> Describe the basic concept of software project management with its life cycle.  <b>CO2-</b> Apply project estimation and evaluation techniques to real world problem	L3  L3
<b>Module II:</b> Activity planning - project schedules - sequencing and scheduling projects - Network planning models - AON and AOA - identifying critical activities - crashing and fast tracking, Risk management: Categories, Risk planning, management and control - Evaluating risks to the schedule, PERT. Resource allocation - identifying resource requirements - scheduling resources - creating critical paths - publishing schedule - cost schedules - sequence schedule. CPM, Gantt chart, staffing, organizing a software engineering project	<b>CO3-</b> Apply project management system techniques like PERT , Gantt chart , AON and AOA  <b>CO4-</b> Identify project risk & monitor	L3  L2
<b>Module III:</b> Monitoring and control – Visualizing progress, Earned value analysis – Managing people and organizing teams – organizational structures - Planning for small projects. Case Studies, Agile Development.	<b>CO5-</b> Work in team to evaluate the different mode of communication among people.	L5
<b>Module IV:</b> Software quality- quality engineering, defining quality requirements, quality standards, practices & conventions, ISO 9000, ISO 9001, Software quality matrices, managerial and organization issues, defect prevention, reviews & audits, SEI capability maturity model, PSP, six sigma.	<b>CO6-</b> Describe different techniques used in monitoring and improve the quality of project	L3



# SYNERGY INSTITUTE OF TECHNOLOGY, BHUBANESWAR

Approved by AICTE, New Delhi, Affiliated to BPUT, Odisha

## Department of Computer Science and Engineering

**Subject: Green Technology**

**Year/Semester: 4<sup>th</sup> / 7<sup>th</sup>**

**Subject Code: C404**

Modules	CO Statement After the successful completion of the course, students will be able to:	BTL
<b>Module 1</b> -Introduction and physical definition of global warming, the New Carbon Problem: Accumulation, Long Half-Life, Heating Potential, Carbon Emission Factors, Carbon Absorption in Nature, The Global Emission Situation and its effect in India, The Kyoto and Other Protocols and its view in India, Effect of climate change and its impact. Planning for the Future to reduce global warming Steps taken to Control Carbon Emissions universally, Use of Promotional and Punitive Mechanisms for Reducing Carbon in Atmosphere.	<b>CO1</b> - Understand the cause of production of greenhouse gases and it is effect on the environment.	L2
<b>Module 2</b> -The General Approach in Planning for the Future, Developing Countrywide Adaptive Measures for Safety of Local People, Developing Mitigate Measures for Global Reduction of Carbon, India's National Action Plan on Climate Change (NAPCC) till date, National Mission for a Green India, The MRV Debate.	<b>CO2</b> - Learn the basic actions to prevent global warming and climate change.	L2
<b>Module 3</b> -Opportunities in Control of Carbon Emissions and Accumulation:- Essential Steps for Control of Carbon Emissions and Accumulation, Procedure to develop own Priorities and Business Opportunities in India for control of carbon emissions and accumulation, Needs a Mix of Green and Traditional Power Sources in India, A Logical Approach for Carbon Reduction, Need in India —More Forests, Less Deforestation and payment rates procedure for controlling carbon emissions and its Promotional Mechanisms at India.	<b>CO3</b> - Understand the impact knowledge on the methods of reducing Co <sub>2</sub> level in atmosphere.	L2
<b>Module 4</b> -Green Technologies for Energy Production:- Various Technologies Available for Energy Production, Cost Comparison of a Few Typical Systems for Power Generation, Sources of Energy Production Already in Use, Alternative Methods Ready for Use, Green Technologies Needing some Prior R&D Work.	<b>CO4</b> - Know the importance of alternative energy sources for energy production	L2
<b>Module 5</b> -Green Technologies for Personal and Citywide Application: - Measures to be taken for Green city, Carbon Emission Reduction at Personal Level, Carbon Emission Reduction at Local Authority and Citywide Level, Carbon Emissions from Imports. Green Technologies for Specific Applications:- Promotion of 'Green' Buildings, Guidelines, The Energy Conservation Building Code (ECBC), Green Hotels and Hospitals,	<b>CO5</b> - Understand the principles of green building technology and energy conservation measures.	L2



# SYNERGY INSTITUTE OF TECHNOLOGY, BHUBANESWAR

Approved by AICTE, New Delhi, Affiliated to BPUT, Odisha

## Department of Computer Science and Engineering

SYNERGY.BHUBANESWAR

Green Technologies for Transport, Green Roads, Ports and Harbors, Industries, Carbon, Carbon Emissions from a Few Selected Industries in India, The Changing Scenario in Cities, Need for Wider Application to Town Planning and Area Re- Development Projects , 'Green' Infrastructure for Municipal Services, Bringing up Indian Villages, Green Services for Crematoria, Spreading Message to all Stakeholders.		
<b>Module 6-</b> Some High-tech Measures for Reducing Carbon Emissions: - Use of Solar Power with Satellite-Based Systems, Use of Carbon Capture and Storage (Sequestration), Microorganisms, A Quick SWOT Analysis. Recommended Plan of Action: - India's National Action Plan Take Us to a Low-Carbon Path, The Missions Help Develop Awareness, few case studies on Projects undertaken by Various Countries, Adaptive Measures Essential for Indian People to Cope with Climate Change	<b>CO6-</b> Learn the measures used on modern technology to reduce the climate change.	<b>L2</b>



# SYNERGY INSTITUTE OF TECHNOLOGY, BHUBANESWAR

Approved by AICTE, New Delhi, Affiliated to BPUT, Odisha

## Department of Computer Science and Engineering

**Subject: Cyber Law and Ethics**

**Year/Semester: 4<sup>th</sup> / 7<sup>th</sup>**

**Subject Code: C406**

Modules	CO Statement After the successful completion of the course, students will be able to:	BTL
<b>Module-I:</b> Introduction to Cyber Law Evolution of computer technology, emergence of cyber space. Cyber Jurisprudence, Jurisprudence and law, Doctrinal approach, Consensual approach, Real Approach, Cyber Ethics, Cyber Jurisdiction, Hierarchy of courts, Civil and criminal jurisdictions, Cyberspace-Web space, Web hosting and web Development agreement, Legal and Technological Significance of domain Names, Internet as a tool for global access.	<b>CO1-</b> Know various aspect of cyber law, ethics & different cyber law approaches like Consensual approach, Real Approach, Cyber Ethics, Cyber Jurisdiction etc.	L1
<b>Module-II:</b> Information Technology Act Overview of IT Act, 2000, Amendments and Limitations of IT Act, Digital Signatures, Cryptographic Algorithm, Public Cryptography, Private Cryptography, Electronic Governance, Legal Recognition of Electronic Records, Legal Recognition of Digital Signature, Certifying Authorities, Cyber Crime and Offences, Network Service Providers Liability, Cyber Regulations Appellate Tribunal, Penalties and Adjudication.	<b>CO2-</b> Know the IT Act, 2000, Amendments and Limitations of IT Act.  <b>CO3-</b> Focus on various aspects of digital transactions & cyber security	L3  L2
<b>Module-III:</b> Cyber Law and Related Legislation Patent Law, Trademark Law, Copyright, Software – Copyright or Patented, Domain Names and Copyright disputes, Electronic Data Base and its Protection, IT Act and Civil Procedure Code, IT Act and Criminal Procedural Code, Relevant Sections of Indian Evidence Act, Relevant Sections of Bankers Book Evidence Act, Relevant Sections of Indian Penal Code, Relevant Sections of Reserve Bank of India Act, Law Relating To Employees And Internet, Alternative Dispute Resolution , Online Dispute Resolution (ODR).	<b>CO4-</b> Know different type of Cyber Law and various legal aspects related to cyber-crime.	L2
<b>Module-IV:</b> Electronic Business and Legal Issues Evolution and development in E-commerce, paper vs paper less contracts E-Commerce models- B2B, B2C, E security. Business, taxation, electronic payments, supply chain, EDI, E-markets, Emerging Trends.	<b>CO5-</b> Understand the evolution and development of e-commerce to significantly transformed how business transactions are conducted	L3
<b>Module-V:</b> Cyber Ethics The Importance of Cyber Law, Significance of Cyber Ethics, Need for Cyber regulations and Ethics. Ethics in Information society, Introduction to Artificial Intelligence Ethics: Ethical Issues in AI and core Principles, Introduction to Block chain Ethics.	<b>CO6-</b> Know the Importance of Cyber Law, Significance of Cyber Ethics, Need for Cyber regulations and Ethics.	L1



# SYNERGY INSTITUTE OF TECHNOLOGY, BHUBANESWAR

Approved by AICTE, New Delhi, Affiliated to BPUT, Odisha

## Department of Computer Science and Engineering

**Subject: Digital VLSI Design**

**Year/Semester: 4<sup>th</sup> / 7<sup>th</sup>**

**Subject Code: C405**

Modules	CO Statement After the successful completion of the course, students will be able to:	BTL
<b>Module-1:</b> Introduction: Historical Perspective, VLSI Design Methodologies, VLSI Design Flow, Design Hierarchy, Concept of Regularity, Modularity and Locality, VLSI Design Styles, Computer-Aided Design Technology.	<b>CO1</b> Understand the concepts of vlsi, design style, Fabrication of NMOS, PMOS and CMOS.	<b>L2</b>
<b>Module-2:</b> Fabrication of MOSFETs: Introduction, Fabrication Processes Flow – Basic Concepts, The CMOS n-Well Process, Layout Design Rules, Stick Diagrams, Full Customs Mask Layout Design. MOS Transistor: The Metal Oxide Semiconductor (MOS) Structure, The MOS System under External Bias, Structure and Operation of MOS Transistor (MOSFET), MOSFET Current-Voltage Characteristics, MOSFET Scaling and Small-Geometry Effects, MOSFET Capacitance.	<b>CO2</b> Explain the application fabrication of MOS Logic Circuits and circuit operation.	<b>L3</b>
<b>Module-3:</b> MOS Inverters – Static Characteristics: Introduction, Resistive-Load Inverters, Inverters with n-Type MOSFET Load, CMOS Inverter. MOS Inverters – Switching Characteristics and Interconnect Effects: Introduction, Delay-Time Definitions, Calculation of Delay-Times, Inverter Design with Delay Constraints, Estimation of Interconnect Parasitic, Calculation of Interconnect Delay, Switching Power Dissipation of CMOS Inverters. Combinational MOS Logic Circuits: Introduction, MOS Logic Circuits with Depletion NMOS Loads, CMOS Logic Circuits, Complex Logic Circuits, CMOS Transmission Gates (Pass Gates).	<b>CO3</b> Design Combinational Circuits using MOS logic circuits and performance analysis.	<b>L2</b>
<b>Module-4:</b> Sequential MOS Logic Circuits: Introduction, Behavior of Bi stable Elements, SR Latch Circuits, Clocked Latch and Flip-Flop Circuits, CMOS D-Latch and Edge Triggered Flip Flop. Dynamic Logic Circuits: Introduction, Basic Principles of Pass Transistor Circuits, Voltage Bootstrapping, Synchronous Dynamic Circuit Techniques, Dynamic CMOS Circuit Techniques, High Performance Dynamic CMOS Circuits.	<b>CO4</b> Design Sequential Circuits using MOS logic circuits and performance analysis.	<b>L2</b>
<b>Module-5:</b> Design for Testability: Introduction, Fault Types and Models, Ad Hoc Testable Design Techniques, Scan-Based Techniques, Built-In Self-Test (BIST) Techniques, Current Monitoring	<b>CO5</b> Understand and apply the VHDL programming of Digital Logic Circuits.	<b>L3</b>



# SYNERGY INSTITUTE OF TECHNOLOGY, BHUBANESWAR

Approved by AICTE, New Delhi, Affiliated to BPUT, Odisha

## Department of Computer Science and Engineering

SYNERGY.BHUBANESWAR

IDDQ Test.		
<b>Module-6:</b> Semiconductor Memories: Introduction, Dynamic Random Access Memory (DRAM), Static Random Access Memory (SRAM), Non-volatile Memory, Flash Memory.	<b>CO6</b> Summarize the Semiconductor memories-DRAM, SRAM, Flash Memory	<b>L2</b>



# SYNERGY INSTITUTE OF TECHNOLOGY, BHUBANESWAR

Approved by AICTE, New Delhi, Affiliated to BPUT, Odisha

## Department of Computer Science and Engineering

**Subject: Minor Project**

**Year/Semester: 4<sup>th</sup> / 7<sup>th</sup>**

**Subject Code: C407**

CO	CO Statements After completion of the course the student will be able to	BTL
CO1	Define problems and suggest a feasible, cost effective, ecofriendly solution for the benefit of the society.	L2
CO2	Understand relation of the project to the literature & Engineering knowledge.	L2
CO3	Demonstrate properly to complete the project within the scheduled time.	L2
CO4	Analyze project with proper methodology and team spirit.	L4
CO5	Evaluate and validate the project.	L5
CO6	Generate thesis/ project report as per standard norm.	L2



# SYNERGY INSTITUTE OF TECHNOLOGY, BHUBANESWAR

Approved by AICTE, New Delhi, Affiliated to BPUT, Odisha

## Department of Computer Science and Engineering

**Subject: Seminar-II**

**Year/Semester: 4<sup>th</sup> / 7<sup>th</sup>**

**Subject Code: C408**

CO	CO Statements After completion of the course the student will be able to	BTL
CO1	Find topics on modern technology, Prepare implementation of the same as the presentation.	L2
CO2	Understand the technologies used by extracting the new things to be implemented by reviewing the journals/research papers.	L2
CO3	Sketch the application of the technology for the use of the mankind.	L2
CO4	Analyze and correlate the new technology with the subject of interest for further study.	L4
CO5	Evaluate, plan and reframe the technology with the communication skills for a better explanation and presentation.	L2
CO6	Modify and design the concept into the realistic world.	L3



# SYNERGY INSTITUTE OF TECHNOLOGY, BHUBANESWAR

Approved by AICTE, New Delhi, Affiliated to BPUT, Odisha

## Department of Computer Science and Engineering

**Subject: Major Project**

**Year/Semester: 4<sup>th</sup> / 8<sup>th</sup>**

**Subject Code: C410**

**After completion of the course the student will be able to**

CO	CO Statements After completion of the course the student will be able to	BTL
CO1	Define problems and suggest a feasible, cost effective, ecofriendly solution for the benefit of the society.	L2
CO2	Understand relation of the project to the literature & Engineering knowledge.	L2
CO3	Demonstrate properly to complete the project within the scheduled time.	L2
CO4	Analyze project with proper methodology and team spirit.	L4
CO5	Evaluate and validate the project.	L5
CO6	Generate thesis/ project report as per standard norm.	L2